



SARANATHAN COLLEGE OF ENGINEERING
Venkateswara Nagar, Panjappur, Tiruchirappalli -12



AICTE sponsored
**International e-Conference on “Cutting Edge Technologies in Electrical,
Communication, Embedded System and Soft Computing Techniques
(ICECES’20)”**

The Departments of Electrical and Electronics Engineering & Electronics and Communication Engineering of Saranathan College of Engineering, Trichy, jointly organized an AICTE sponsored Two-Day International e-Conference on **“Cutting Edge Technologies in Electrical, Communication, Embedded System and Soft Computing Techniques (ICECES ’20)”**, on **5th November & 6th November 2020**. The theme of this conference was envisaged keeping in mind the role of technology and/or computing play in building the knowledge society. 143 papers, out of the 150 received for the conference, were presented. Around 300 students, research scholars and faculty members from various Engineering colleges participated in this presentation of their research findings in this virtual e-Conference.

On 5th November 2020, the inaugural function started with the lighting of the “kuthuvilakku” by Dr.D.Valavan – the Principal, Convenors,, Dr.C.Krishnakumar – HOD/EEE & Dr.M.Santhi – HOD/ECE, Programme Coordinators, Dr.C.Vennila - Professor/ECE and Dr.S.Vijayalakshmi - Associate Professor/EEE.

Dr.S.Vijayalakshmi, welcomed the gathering and the Principal, Dr. D. Valavan delivered the presidential address. The inaugural ceremony witnessed the address by eminent keynote speaker, Dr.Seok-Bum Ko, Professor, University of Saskatchewan, Canada. He enlightened the participants with his insightful speech on the significance of research in VLSI incorporated with Deep Neural Networks, Machine Learning, Image Processing. He reiterated the need for Deep Learning, Quality education, integrity and utmost dedication in the research in this field of cutting-edge technology.

The inaugural ceremony concluded with a vote of thanks given by Dr.C.Vennila. The inaugural function was followed by paper presentation sessions of various departments. They were presented in the virtual mode and in a multi-track fashion.

In Track 1, Dr.A.Nazar Ali, Associate Professor/EEE, Rajalakshmi Engineering College, Chennai, performed the role of judge/jury in the FN session while Dr.K.Dhayalini, Professor & Head/EEE, K.Ramakrishnan College of Engineering, Trichy did that role in the AN session.

In Track 2, Dr.J.Manikandan, Professor, Crucible of Research and Innovation (CORI), PES University, Bangalore was the judge/jury in FN session while Dr.K.Swaminathan, Head-FPGA Design Team, Jiva sciences Pvt. Ltd, Bangalore, Karnataka,in the AN session.

In Track 3, Dr.R.Gandhimathi, Assistant Professor/Civil, NIT, Trichy was the judge/jury in FN session.

The second day of the conference, 6th November 2020, saw the keynote address being delivered by Dr.Y.Venkataramani, Dean (R&D), Saranathan College of Engineering, Trichy. He presented a very informative session about the significance of Machine Learning in Wireless & 5G Communication. A vote of thanks was delivered by Dr.M.Santhi, Convener, HoD/ECE. The virtual paper presentation sessions followed in a virtual fashion.

In track 1, Dr.G.Kannan, Associate Professor/ECE, B.S.Abdur Rahman Crescent Institute of Science & Technology, Chennai, took on the role of judge/jury in the FN session while Dr.R.Shenbagalakshmi, Professor/EEE, SKN Sinhgad Institute of Technology & Science, Lonavala, Maharashtra, performed that role in the AN session.

In track 2, Dr.M.Maheswari, Professor & Head/ECE, K.Ramakrishna College of Engineering, Trichy, was the judge/jury in FN session while Dr.R.Rajeswari, Professor/ECE, Rajalakshmi Institute of Technology, Chennai, took on that role in AN session.

In track 3, Dr.Jasmine Beulah Gnanadurai, Professor, Kristu Jayanti College, Bangalore was the judge/jury in FN session.

In all there were 10 paper presentation sessions spread over 2 days/4 sessions/ 3 tracks. In each of these sessions two faculty members from Saranathan were also present in the jury panel. On both the days of the e-conference, track wise – session wise the jury adjudged the best paper presented. A “Best paper award” was awarded to each one of these presentations.

The conference received positive feedback from all the participants as it provided the ideal platform to the students and research scholars to share their research findings in a forum that had veteran academicians.



Dignitaries on the Dias



**Dr.S.Vijayalakshmi, Associate Professor, Programme Coordinator/ EEE,
delivered welcome address**




Lighting of Kuthuvilaku in the Conference



Dr.D.Valavan, Principal, delivered presidential address

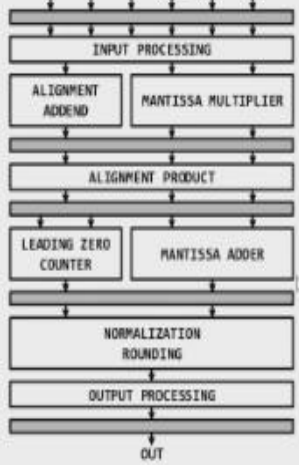
REC

Multiple-Precision Fused Multiply-Add Unit







UNIVERSITY OF
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Functions Supported By The Proposed Unit:





- Normal FMA operations:
 - 1 QP FMA
 - 2 parallel DP FMA
 - 4 parallel SP FMA
 - 8 parallel HP FMA
- Mixed-precision dot-product:
 - 4 parallel: 2 HP products + 1 SP addend
 - 2 parallel: 2 SP products + 1 DP addend
 - 2 DP products + 1 QP addend
- Mixed-precision FMA operations:
 - 4 parallel: HP product + SP addend
 - 2 parallel: SP product + DP addend
 - DP product + QP addend






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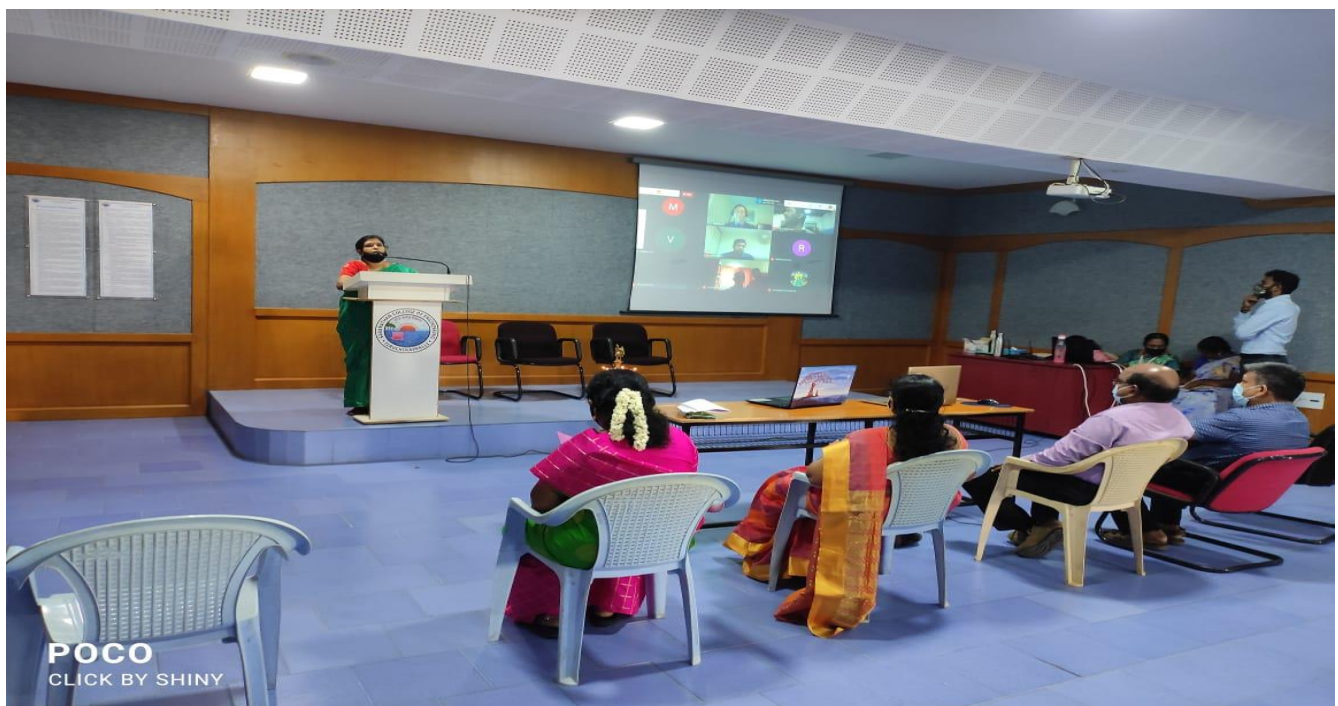



Mr.Dinesh

56 others

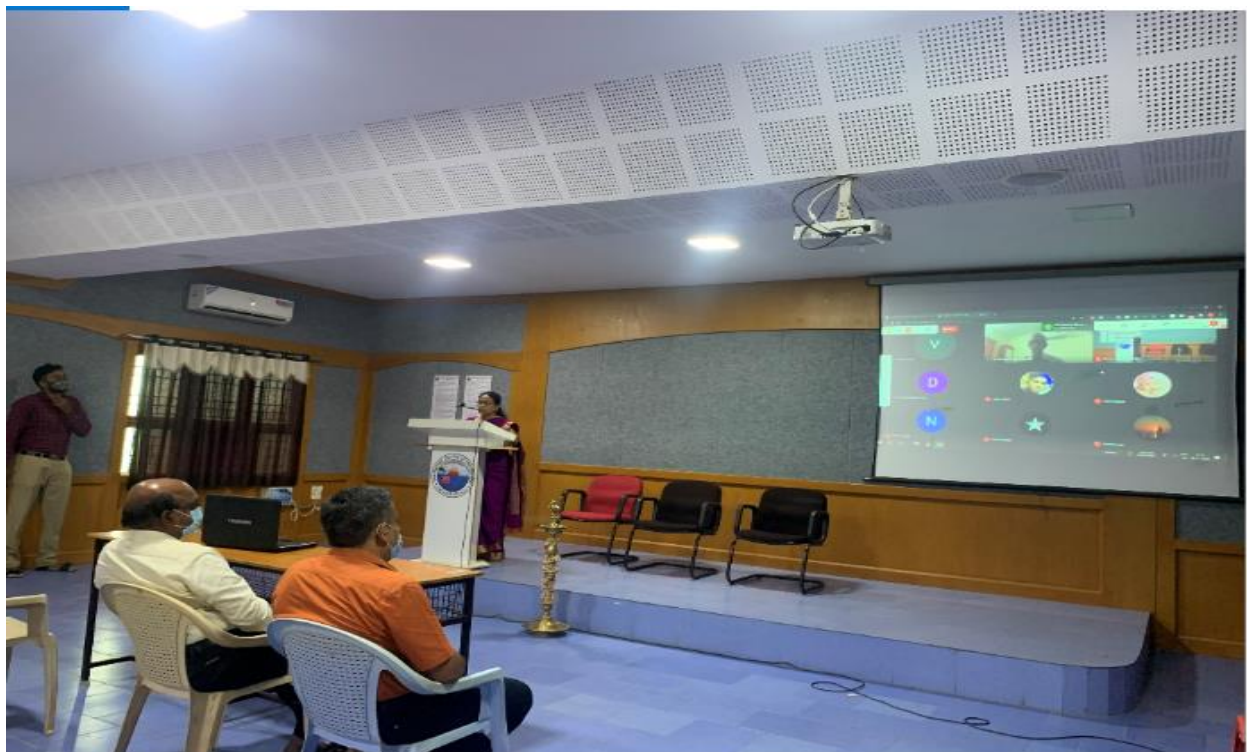
Dr.Seok-Bum Ko, Professor, University of Saskatchewan, Canada delivered keynote address



Dr.C.Vennila, Professor, Programme Coordinator/ ECE, delivered vote of thanks



Dr.Y.Venkataramani, Dean (R&D), Saranathan College of Engineering, Trichy, delivered the keynote address



Dr.M.Santhi, HoD/ECE ,Convener, delivered vote of thanks