



**SARANATHAN COLLEGE OF ENGINEERING**  
(Approved by AICTE, New Delhi, Affiliated to Anna University, Chennai-25)

**INDEX**  
**Criterion 2.5.1**

**2.5.1 Mechanism of internal assessment is transparent and robust in terms of frequency and mode**

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## Theory Courses

SARANATHAN COLLEGE OF ENGINEERING DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING INTERNAL ASSESSMENT TEST - 1 : ODD SEMESTER 2019 SEATING ARRANGEMENT						
HALL	II CSE - A	II CSE - B	III CSE - A	III CSE - B	IV CSE - A	IV CSE - B
KS 101			201001-18			
KS 109			19-22,24,26-38			
KS 110			39,40,42-56			
KS 111			57-60,119-128	201061,63-69		
KS 113				70,72-86		
KS 114				87-104		
KS 213				105-125		
KS 214				129		
RV 406	211001-03					
RV407	04 -- 20					
RV 409	21-36					
RV410	37-52					
RV 412	53-61	211062-71				
ME 302		72-95				
ME 303		96-117				
ME 402		118-121				
ME 301					191001-12	
ME 401					13-34	
RV 413					35-50	
RV 414					51-57,118,121	191058-65
RV 415						67-84
RV 116-A						85-101
js102						102& 103
JS C.H. -1						104-122
<div style="display: flex; justify-content: space-between; align-items: center;"> <div>             [EXAM COORDINATOR]         </div> <div>             [HOD]         </div> </div>						



Date: 07 Jan 2018

## Department of Computer Science and Engineering

### First Class Committee Meeting Report for II year CSE – 'A' Section

The First Class Committee Meeting for II year CSE - 'A' Section was held on 5<sup>th</sup> January 2018 at 12.30 pm with the following Staff members and Student representatives and the below mentioned points were discussed in the meeting.







Vision, mission of the institution, department and POs, PEOs, PSOs were disseminated

**Chair Person** : Dr. S.A.Sahaaya Arul Mary, HOD, Department of CSE

**Class Coordinator** : Mr.R. Mohankumar, AP/CSE.

**Venue** : CSE - Main Computer Lab

Sl No	Sub Code	Subject Title	Minutes	Name of the Staff	Staff Signature
1	CS6401	Operating Systems	The students are comfortable with the subject. Students requested to give notes for the subject	Dr.P.L.Rajarajeswari (CSE)	
2	CS6402	Design and Analysis of Algorithm	The students were able to understand the subject easily and they felt that this subject is useful.	Mr. M. Anbazhagan (CSE)	
3	CS6403	Software Engineering	The students are comfortable with the subject and feel easy to understand the concepts delivered by the staff	Ms. R. Lavanya Bai (CSE)	

Sl No	Sub Code	Subject Title	Minutes	Name of the Staff	Staff Signature
4	CS6551	Computer Networks	The students are able to understand the subject easily and feel easy to correlate with the real-time applications.	Mr.R. Mohankumar (CSE)	
5	EC6504	Micro Processors and Micro Controllers	The Students are comfortable with the subject which taught by the staff. The material given by the staff is useful and self-explanatory.	Ms.M.Anthuwan Lydia (ECE)	
6	MA6453	Probability and Queuing Theory	The students are able to solve the problem by their own. They requested to give extra questions to solve in home.	Dr. P.K Lakshmidevi (MATHS)	
7	CS6411	Networks Lab	The students are able to understand the concept and to code it in the lab. They requested to show demo for difficult exercises	Mr.P.Dineshkumar (CSE) / Mr.R.Mohankumar (CSE)	
8	CS6412	Micro Processors and Micro Controllers Lab	The students are able to understand the concept and to <u>implement it</u> in the lab.	Ms.M.Anthuwan Lydia (ECE)	
9	CS6413	Operating Systems Lab	The students are able to understand the concept and to code it in the lab.	Dr.S.Mohana (CSE)/ Dr.P.L.Rajarajeswari (CSE)	



**The following points were discussed by the Chair Person:**

1. The students are insisted to maintain the class room as well as college premises clean and neat.
2. The students are instructed to avoid the absenteeism.
3. The class room desks should be arranged in proper manner as per the instructions given by the Principal.
4. The Staff members should take additional care on slow-learning students to improve their knowledge as well as the result.
5. The Staff members should insist the students to maintain their attendance percentage above 80 Percent by avoiding, taking leave and in unavoidable conditions, they should inform their Class coordinator and HOD well in advance.

**The requisitions made by the students:**

1. The Students requested the HOD for Industrial Visit and it was instructed by HOD that, it will be considered on the basis of performance in IA Test 1 and IA Test 2.
2. The Students requested to conduct various workshops, seminars and coding context, which will be very useful to improve their technical knowledge.

**Student Members:**

Sl.No	Name of the Students	Student's Signature
1	ARAVIND APARAJITH K (191008)	Aravind Aparajith K
2	HARINI R (191032)	H.R.
3	JANANI R (191036)	R Janani
4	KEERTHIKA S (191049)	S. Keerthika
5	KISORE N.B (191050)	N.B. Kisore
6	KISHORE S (191051)	S. Kishore
7	ANU PREETHA T (191117)	T. Anupreetha
8	KEERTHANA B (191046)	B. Keerthana
9	MADHUBALA S (191052)	S. Madhubala
10	VEERARAGHAVAN B (191121)	B. Veeraraghavan



CLASS COORDINATOR

(R. Manan Kumar)



HOD/CSE



PRINCIPAL

# **SARANATHAN COLLEGE OF ENGINEERING, TRICHY 12**

## **DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**

### **MINUTES OF MEETING**

**DATE: 13/07/2019**

**VENUE: RV-401/CSE-FACULTY ROOM**


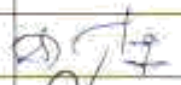















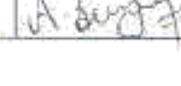


The following points were discussed in the staff meeting.

1. The Mission and Vision of the college and department is disseminated to the staff members.
2. The first year Management Quota regular classes will commence from 15<sup>th</sup> of this month with five sections, and it is suggested to cover the portions very gradually.
3. It is also informed that the Induction Programme for the students admitted through counseling quota will be from 29<sup>th</sup> July 2019 to 2<sup>nd</sup> August 2019 and regular classes for those students will commence from 5<sup>th</sup> August 2019.
4. It has been decided to conduct one IA test per day and the test will be conducted during the 1<sup>st</sup> and 2<sup>nd</sup> hours for the IA-1 test followed by the regular classes from 3<sup>rd</sup> hour.
5. HOD informed that two IA Tests with one and half units each and one Model Exam with all five units will be conducted. Internal marks will be awarded with 25% of mark weightage for each IA test and 50 % of mark weightage for model exam.
6. The department wise review on IA Test performance of the students will be on the following days of the last test.
7. Record and observation notebooks have to be completed and corrected periodically. Furthermore correction of record work and observation should not be done during lunch interval or at regular intervals.
8. Staff members are advised to ensure that the students have hard copy of study material of their respective subjects.
9. Management is very happy to inform that the college is planning to apply for NAAC and Autonomous status this year.

10. Project Day will be conducted for the second and third year students on 13<sup>th</sup> and 14<sup>th</sup> of September 2019. Maximum of five students can be in a team. HOD insisted that from each section there should be at least 5 teams to be registered. The students can select their faculty guide for their project. It is also announced that prizes will be given to the deserving projects based on the scores given by the jury panel from corporate and industry.

  
HOD-CSE

**Faculty Signature:**

S. No.	Staff Name	Signature	S. No.	Staff Name	Signature
1	Dr.S.A.Sahaaya Arul Mary		13	Mr.R.Mohan Kumar	
2	Dr.S.Mohana		14	Mr.P.Dinesh Kumar	
3	Dr.P.L.Rajarajeswari		15	Ms.N.Kavitha	
4	Mr.S.Venkatasubramanian		16	Mr.D. Boobala Muralidharan	
5	Ms.V.Punitha		17	Mr.M. Anbazhagan	
6	Ms.R.Thillaikarasi		18	Mr.R.Karthik	
7	Mr.K.S.Chandrasekaran		19	Mr.P.B.Arunprasad	
8	Ms.R.Senthamilselvi		20	Ms. R.Sharanya	
9	Mr.T.SathisKumar		21	Ms. N. Saipraba	
10	Ms.K.Mohanapriya				
11	Ms.J.SathiaParkavi				
12	Mr.A.T.BaraniVijaykumar				





## SARANATHAN COLLEGE OF ENGINEERING

Venkateswara Nagar, Panjappur, Tiruchirappalli – 620 012.






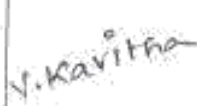

Date: 06.01.2018

### Department of Computer Science and Engineering

#### First Class Committee Meeting Report for II year CSE – ‘B’ Section

The first Class Committee Meeting for II year CSE - ‘B’ Section was held on 6<sup>th</sup> January 2018 at 2.30pm with the following Staff Members and Student representatives and the below mentioned points were discussed in the session.

<b>Chair Person : Dr.S.A.Sahaaya Arul Mary, HOD, Department of CSE.</b> <b>Class Coordinator : Mr.D.Boobala Muralitharan, AP/CSE.</b> <b>Venue : CSE - Main Lab.</b>					
Sl.No	Subject Code	Subject Title	Minutes	Staff Name	Staff Signature
1.	MA6453	Probability and Queuing Theory	The students are comfortable with the subject and they felt that the teaching speed can be decreased.	Mr.S.Sivamani (MATHS)	
2	CS6551	Computer Networks	The students are able to understand the subject easily.	Mr.P.Dineshkumar (CSE)	
3	CS6401	Operating Systems	The students are comfortable with the subject. Students requested to take class bit faster.	Dr.S.Mohana (CSE)	
4	CS6402	Design and Analysis of Algorithm	The students were able to understand the subject easily and they felt that this subject is useful for updating their Algorithmic knowledge and able to write algorithm in own, because of Staff's Teaching.	Ms.R.Thillaikarasi (CSE)	

Sl.No	Subject Code	Subject Title	Minutes	Staff Name	Staff Signature
5	EC6504	Micro Processors and Micro Controllers	The students are comfortable with the subject understanding. Students gave feedback that the surprise open book test being conducted in this subject is very useful.	Ms.V.Kavitha (ECE)	
6	CS6403	Software Engineering	The students are able to understand the subject easily due to the interaction between Staff and Students. The students felt that they can update their knowledge without any obstacles.	Mr.D.Boobala Muralitharan (CSE)	
7	CS6411	Networks Lab	The students are able to understand the concept and to code it in the lab.	Mr.P.Dineshkumar (CSE) / Mr.R.Mohankumar (CSE)	
8	CS6412	Micro Processors and Micro Controllers Lab	The students are able to understand the concept and to implement it in the lab.	Ms.V.Kavitha (ECE) / Mr.V.Saravanan (ECE)	
9	CS6413	Operating Systems Lab	The students are able to understand the concept and to code it in the lab.	Dr.S.Mohana (CSE)/ Dr.P.L.Rajarajeswari (CSE)	

**The following points were discussed by the Chair Person:**











1. Vision, mission of the institution, department and POs, PEOs, PSOs were disseminated
2. The students should be motivated to maintain discipline in Class room as well as inside the College premises.
3. The students should be instructed to avoid the late presence, by the Staff members.
4. The Class room desks should be arranged in proper manner as per the instructions given by the Principal.
5. The Staff members should take additional care on weak students to reduce the number of failures.
6. The Staff members should insist the students to maintain their attendance percentage above 80 Percent by avoiding, taking leave and in unavoidable conditions, they should inform their Class coordinator as well as HOD, in advance.



**The requisitions made by the students:**

1. The Students requested to conduct various coding context in the inter-department level, which will be very useful to improve their coding knowledge.
2. The Students requested for Industrial Visit. And it was instructed by HOD that, it will be considered on the basis of first and second IA Test performance.

**Student Members:**

Sl.No	Name of the Students	Student's Signature
1	Rohit Raj	
2	Roshini Raj. M	
3	Nandhini. E	
4	Nandha Gopala Krishnan. C	
5	Prakash. V	
6	Panjani. K	
7	Renuka. R	
8	Revathi. A.U	
9	Sanjay. D	
10	Mohana Priya. S	

  
CLASS COORDINATOR  
D. Boobala mukali Dahan

  
HOD/CSE

  
PRINCIPAL

10/1/18

STUDENT'S PERSONAL RECORD [MQ/GQ] [R/LE]

1. NAME OF THE STUDENT : M.J.GOKULAKRISHNAN

2. DEPARTMENT : ECE

3. UNIVERSITY REGISTRATION No : .

4. COMMUNITY : BC

5. DATE OF BIRTH : 29.08.2000

6. ADDRESS (PERMANENT) : (FOR COMMUNICATIONS)

536, RAILWAY QUARTERS,  
KALLUKUZH1.  
TRICHY. 620 020

7. NAME OF THE LOCAL GUARDIAN : —  
(in the case of outstation students)

ADDRESS & PHONE :

8. HOSTELER/DAY SCHOLAR/PRIVATE : —  
(Provide private hostel address)

9. BLOOD GROUP : A +ve



PHOTO  
of Local  
Guardian

  
STUDENT'S SIGNATURE

STUDENT'S ADVISOR

  
PARENT'S SIGNATURE

S#	Course Code	Course Title	Name of the Staff	ATTENDANCE		
				Periods Conducted	Periods Attended	% of Attendance
1	HS8251	Technical English	Ms. M. Premalatha	70	55	78.57
2	MA8254	Engineering Mathematics II	Dr. S. Revathy	80	59	73.75
3	PH8263	Physics for Electronics Engineering	Mr. P. Saravanan	71	53	74.65
4	BE8252	Basic Electrical & Electronics Engineering	Ms. R. S. Priyadharshini	73	57	78.08
5	EL8251	Circuit Analysis	Ms. A. Shamin Banu	87	53	66.67
6	EL8252	Electronic Devices	Mr. S. Hariprasad	73	51	69.86
7	EL8261	Circuit Devices Lab	Mr. S. Hariprasad	76	73	96.05
8	GE8261	Engineering Practice Lab (Group A)	Mr. S. Vinodh Kumar	83	70	84.34
9	GE8261	Engineering Practice Lab (Group B)	Ms. R. S. Priyadharshini Ms. A. Shamin Banu			

AVERAGE ATTENDANCE %: 77.65%

## COUNSELLING RECORD :

(AFTER EACH I.A. TEST)

I.A.#	Remarks about I.A. Performance	Student's Feedback	Suggestions/Advice of Mentor
1	Not good	Didnot prepare well	Come regularly to college
2	Not good	didnot prepare well due to sickness.	Concentrate in solving problems
3	Not good	Not well.	Study well for UNO exams

S#	I. A. TEST MARKS (MAX : 100 MARKS)							UNIVERSITY EXAM RESULTS					
	IA1	RT1	IA2	RT2	IA3	RT3	Model Exam.	ATTEMPT 1		ATTEMPT 2		ATTEMPT 3	
								Grade Obtained	Pass / Fail	Grade Obtained	Pass / Fail	Grade Obtained	Pass / Fail
1	74		76		66			A					
2	54	42	1	6	34	0		U		U			
3	18	A	18	12	16	10		U		U			
4	4	14	8	20	12	4		U		UA			
5	10	10	24	30	A	A		UA		U			
6	42	46	16	12	A	A		U		U			
7								B					
8								A					
9													
								GPA	CGPA	GPA	CGPA	GPA	CGPA

ACHIEVEMENTS : (CO-CURRICULAR, EXTRA CURRICULAR, SPORTS ETC.,)  
(Special remarks should be marked in red color)

REMARKS :

\* Advised to attend classes without taking leave and to work hard to clear backlog papers.

STUDENT'S ADVISOR

PARENT'S SIGNATURE

MENTOR

HOD



**Saranathan College of Engineering, Trichy**  
**Portal Entry - I / Class:ECE Semester:6 Section:'B' for the Period from 11-Dec-2019 to 11-Jan-2020**

S.No	Batchno	Regno	Name	Sec	EC8691		EC8095		EC8652		MC8591		EC8651		CS8752	
					Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs
1	202117	813817106031	JOE ANRANGE, A.M	B	17/17	17/16	13/13	13/12	20/21	13/12					13/12	
2	202062	813817106060	PRASANTH, R	B	17/17	17/17	13/13	13/13	20/22	13/13					13/13	
3	202063	813817106061	PREETHI, M	B	17/17	17/17	13/13	13/13	20/22	13/13					13/13	
4	202064	813817106062	PREMCHANDR, S	B	17/12 *	17/14	13/10	13/10	20/17	13/9 *						
5	202065	813817106063	PRINCE, P	B	17/16	17/17	13/11	13/13	20/22	13/13						
6	202066	813817106064	PRITHVIAKSHINI, K	B	17/17	17/16	13/13	13/13	20/22	13/13						
7	202067	813817106065	PRITHVIAKSHINI, T	B	17/17	17/17	13/13	13/13	20/22	13/13						
8	202068	813817106066	RAGHUNATH, K.S	B	17/17	17/14	13/13	13/11	20/21	13/11						
9	202069	813817106067	RAHUL, B	B	17/17	17/17	13/13	13/13	20/22	13/13						
10	202070	813817106068	RAJULAKSHMI, K	B	17/17	17/17	13/13	13/13	20/22	13/13						
11	202071	813817106069	RAKSHI, R	B	17/17	17/16	13/13	13/13	20/22	13/13						
12	202073	813817106071	RASHEED KALU, G	B	17/17	17/15	13/13	13/11	20/21	13/11						
13	202074	813817106072	REASHERA, B	B	17/15	17/14	13/12	13/11	20/21	13/12						
14	202075	813817106073	REHANA BEGUM, A	B	17/16	17/17	13/13	13/13	20/21	13/12						
15	202076	813817106074	RENITA BLESSY, B	B	17/17	17/17	13/13	13/13	20/22	13/13						
16	202078	813817106076	ROSHINI, R	B	17/16	17/17	13/12	13/12	20/21	13/12						
17	202079	813817106077	SAKTHI, T	B	17/17	17/17	13/13	13/11	20/22	13/13						

Produced H.C



S.no	Batchno	Regno	Name	Sec	EC8691		EC8095		EC8652		MG8591		EC8651		CS8792	
					Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs
18	202080	813817106078	SANTHOSHVARAN, G	B	17/14	17/15	13/10	13/11	22/21	13/12						
19	202081	813817106079	SARANYA, M	B	17/17	17/17	13/13	13/13	22/22	13/13						
20	202082	813817106080	SATHYAKRISHNAN, P	B	17/16	17/16	13/12	13/12	22/21	13/12						
21	202083	813817106081	SHAHIN SULTHANA, M .W	B	17/17	17/17	13/13	13/13	22/22	13/13						
22	202084	813817106082	SHAIK AYISHA KAREEMA.	B	17/14	17/13	13/10	13/11	22/17	13/12						
23	202085	813817106083	SHANMUGASUNDARAM, R	B	17/17	17/17	13/13	13/13	22/22	13/13						
24	202086	813817106084	SHARON FRANCIS, A	B	17/16	17/16	13/13	13/12	22/20	13/11						
25	202087	813817106085	SHARON ROSE ROOBALATHY, S	B	17/17	17/17	13/13	13/13	22/22	13/13						
26	202088	813817106086	SHARUNATHY, S. B	B	17/17	17/17	13/13	13/13	22/22	13/13						
27	202089	813817106087	SIVASANKAR, N	B	17/17	17/17	13/13	13/13	22/22	13/13						
28	202090	813817106088	SEVASRI, M	B	17/17	17/15	13/13	13/14	22/21	13/11						
29	202091	813817106089	SOMNATH, K	B	17/17	17/16	13/13	13/13	22/22	13/13						
30	202092	813817106090	SRI JAYA ROHINI, J	B	17/17	17/17	13/13	13/13	22/22	13/13						
31	202093	813817106091	SRI SWAGNA, G	B	17/17	17/16	13/13	13/13	22/22	13/13						
32	202094	813817106092	SUBHASHINI, S	B	17/17	17/16	13/13	13/13	22/21	13/12						
33	202095	813817106093	SUBRAMANI, G	B	17/17	17/14	13/13	13/12	22/22	13/13						
34	202096	813817106094	SUNITHA, B	B	17/17	17/17	13/13	13/13	22/22	13/13						

S.No	BatchNo	Regno	Name	Sec	EC8691	EC8095	EC8652	MG8591	EC8651	CS8782
					Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs
35	202097	813817106095	SOUTHAKA, N	B	17/17	17/16	18/13	18/12	22/21	18/12
36	202098	813817106096	SHAKTI, K	B	17/17	17/16	18/13	18/13	22/22	18/13
37	202100	813817106097	SHAKTI, S	B	17/17	17/15	18/13	18/12	22/20	18/13
38	202101	813817106098	SYED RAJIB, N	B	17/15	17/14	18/12	18/10	22/19	18/10
39	202102	813817106099	TAMILIN KALAR, A.K	B	17/17	17/17	18/13	18/13	22/22	18/13
40	202103	813817106100	TINA TIRANY EDIRA, I	B	17/15	17/15	18/12	18/11	22/21	18/12
41	202104	813817106101	THARISA SUGI	B	17/16	17/17	18/13	18/13	22/21	18/12
42	202105	813817106102	WIDHANE, T	B	17/15	17/14	18/9 *	18/11	22/19	18/12
43	202106	813817106103	WIDHANE, T	B	17/17	17/17	18/13	18/13	22/22	18/13
44	202107	813817106104	WANTHA, K	B	17/17	17/17	18/13	18/13	22/22	18/13
45	202108	813817106105	WASHA DHANASRI, S	B	17/17	17/17	18/13	18/13	22/22	18/13
46	202109	813817106106	WASIM BHASQAR	B	17/13	17/14	18/10	18/10	22/19	18/11
47	202110	813817106107	WEDKA LAKSHMI, K	B	17/17	17/17	18/13	18/12	22/22	18/12
48	202111	813817106108	WEPARAJAYAN, N	B	17/16	17/14	18/12	18/10	22/20	18/11
49	202112	813817106109	WENOCHINI, N	B	17/17	17/16	18/13	18/12	22/20	18/13
50	202113	813817106110	WISWANATHAN, C	B	17/16	17/15	18/13	18/12	22/20	18/11
51	202114	813817106111	WALINI, E	B	18/14 *	17/13	18/8 *	18/8 *	22/14 *	18/9 *

Went to University Exam.

Went to University Exam.



S.No	Batch No	Regno	Name	Sec	EC8691		EC8095		EC8652		MCS591		EC8651		CS8792	
					Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs	Tot.Hrs/ Att.Hrs
52	202115	813817106112	VASCOOKA, N	B	17/11	17/17	13/13	13/13	13/13	13/13	22/22	13/13				
53	202116	813817106113	VELAMMALU VENKATA NAGA ANJANI JYOTHI.	B	17/15	17/16	13/11	13/12	22/20	13/11						
54	202122	813817106301	ANISWARYA, D	B	17/0 *	17/0 *	13/0 *	13/0 *	22/0 *	13/0 *						
55	202120	813817106303	KAVUSHIKA, B	B	17/17	17/16	13/13	13/12	22/21	13/12						
56	202119	813817106305	PADMA SMOUTHI, S	B	17/17	17/17	13/13	13/13	22/22	13/13						
57	202125	813817106701	ZIAD AHMED, M	B	17/12 *	17/12 *	13/8 *	13/8 *	22/15 *	13/8 *						
58	202124	813817106702	NISHANTHONI, B	B	17/13	17/13	13/8 *	13/9 *	22/13 *	13/8 *						
			Staff Signature													

HOD/EC

11.02.2020

Principal

Produced me  
Went to University  
Exam.

Long Absent from  
Part Sem



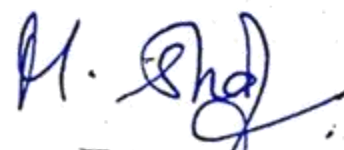
**SARANATHAN COLLEGE OF ENGINEERING**  
(Approved by AICTE, New Delhi, Affiliated to Anna University, Chennai-25)  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

**TIMETABLE FOR REMEDIAL -1(ODD 2018-2019)**

DATES	II ECE - A & B
06.08.18	MA8352 LINEAR ALGEBRA & PARTIAL DIFFERENTIAL EQUN. A-SEC. Dr.B.VENKATESH B-SEC Ms.N.SUBHASHINI
07.08.18	EC8393 FUNDAMENTALS OF DATA STRUCTURES IN C A -SEC Ms.K.MOHANA PRIYA B-SEC Ms.H.SUGANYA
08.08.18	EC8351 ELECTRONICS CIRCUITS - I A-SEC Dr.M. BARITHA BEGUM B-SEC Mr.SRIRAMAN BALAJI
09.08.18	EC8352 SIGNALS & SYSTEMS A-SEC Dr.M. SANTHI B-SEC Dr.V.MOHAN
10.08.18	EC8392 DIGITAL ELECTRONICS A-SEC. Ms.J.EINDHUMATHY B-SEC. Ms.V. RAMYA
13.08.18	EC8391 CONTROL SYSTEM ENGINEERING A-SEC Mr.SATHISHKUMAR B-SEC Ms.N.SHOBANA

DATES	III ECE - A & B
06.08.18	EC6502 PRINCIPLES OF DIGITAL SIGNAL PROCESSING A-SEC Mr.G.SIVAKANNU B-SEC Dr.M.PADMAA
07.08.18	GE6351 ENVIRONMENTAL SCIENCE & ENGG. A-SEC Dr.R.MAHESWARI B-SEC Dr.S.PRIYA REGA
08.08.18	EC6504 MICROPORCESSORS & MICROCONTROLLERS A-SEC Mr.V.DINESH B-SEC Dr.C.VENNILA
09.08.18	EC6503 TRANSMISSION LINES & WAVEGUIDES A-SEC Dr.P.SHANMUGAPRIYA B-SEC. Ms.G.LAKSHMI
10.08.18	EC6501 DIGITAL COMMUNICATION A-SEC Mr.S.HARIPRASATH B-SEC Dr.M.BARITHA BEGUM

DATES	IV ECE - A & B
06.08.18	EC 6701 RF & MICROWAVE ENGG A-SEC Dr.S.A.ARUNMOZHI B- SEC Mr.SD.SAIRAM
07.08.18	EC 6007 SPEECH PROCESSING A-SEC Dr.P.SHANMUGHAPRIYA B-SEC Ms.R.ANILA
08.08.18	EC6004 SATELITE COMMUNICATION A-SEC Mr.S.SARAVANAN B-SEC Mr.S.HARIPRASATH
09.08.18	EC6016 OPTO ELECTRONICS DEVICES A-SEC Ms.G.LAKSHMI B-SEC Dr.C.VENNILA
10.08.18	EC 6703 EMBEDDED & REAL TIME SYSTEMS A-SEC Ms.R.DEVI B-SEC Ms.P.SIVAGAMASUNDARI
13.08.18	EC 6702 OPTICAL COMMUNICATION & NETWORKS A-SEC Dr.S.RAJESWARI B- SEC Mr.K.MALAISAMY

  
HoD/ECE





**Saranathan College of Engineering**  
Tiruchirapalli

Internal Assessment Test - II			Date/ Session	Marks	50
Course code	CS6701	Course	CRYPTOGRAPHY AND NETWORK SECURITY		
Batch No.		Title		Academic Year	2019-2020
Year/	IV	Duration	90 mins	Department	IT
		Semester/ Section	VII		

**Part - A (Answer all the questions 10 x 2 = 20 marks)**

Q. No.	Questions	CO	Skills
1	What is the need of AES?	CO701.2	R
2	List out the authentication requirements.	CO701.3	R
3	What is hash function?	CO701.3	R
4	Define Primitive Root.	CO701.2	R
5	List the uses of RC4/RC5.	CO701.3	R
6	Write any two difference between MD4/5 and Secure hash algorithm	CO701.3	U
7	Define Discrete logarithm	CO701.3	R
8	What is one way property?	CO701.3	R
9	Write disadvantages of Double DES	CO701.2	U
10	Write advantage and disadvantage of OFB mode.	CO701.2	U

**Part - B (Answer all the questions 2 x 10 = 20 marks)**

Q. No.	Questions	CO	Skills
11	Explain the Diffie hellman key exchange algorithm with an suitable example with its pros and cons	CO701.3	R
	OR		
12	Explain Secure hash algorithm in detail.	CO701.3	R
13	Explain about RSA algorithm with example as: P=11,q=5,e=3 and PT=9	CO701.2	C
	OR		
14	Explain how public keys are distributed and managed in public key cryptography.	CO701.2	U

**Part - C (Answer all the questions 1 x 10 = 10 marks)**

Q. No.	Questions	CO	Skills
15	Explain Symmetric block cipher algorithm with plain text 128 bits and it is copied into the state array. And in detail explain how state array is modified at each stage in both encryption and decryption	CO701.2	U
	OR		
16	Analyze how the following modes of block cipher can provide data security for various applications. i. CBC ii. CFB iii. OFB	CO701.2	AZ

*Bloom's Skills: Remember (R), Understand (U), Apply (A), Analyze (AZ), Evaluate (E) and Create (C). Skill need to be mentioned against each question*

Name of the faculty: Ms. J. Sangeethapriya/AP-IT

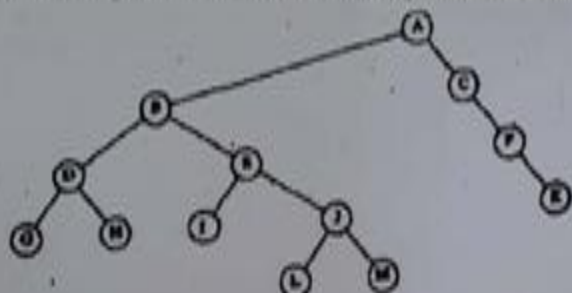
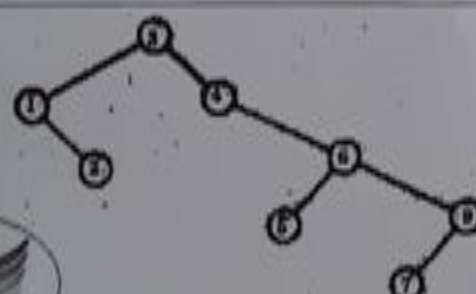
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Tiruchirappalli

Internal Assessment Test - II			Date/ Session	19.08.19 / FN	Marks	50
Course code	CS8391	Course Title	Data Structures			
Batch No.		Duration	90 mins	Academic Year	2019-2020 Odd	
Year/	II	Semester/ Section	III	Department	IT	

**Part - A**  
(Answer all the questions 10 x 2 = 20 marks)

Q. No.	Questions	CO	Skills
1	State the advantages of Doubly linked list.	1	U
2	What is a circular linked list?	1	R
3	Write a routine to implement push operation in stack using linked list	1	U
4	What is radix sort?	1	R
5	List the siblings of node E and Compute the height. 	3	A
6	 Show the result of inorder traversal of the binary tree.	3	A
7	Define depth of a tree.	3	R
8	List the applications of trees.	3	U
9	Define a binary search tree. Give an example.	3	R
10	Define balance factor of AVL Tree.	3	R

**Part - B (Answer all the questions 2 x 10 = 20 marks)**

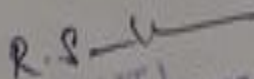
Q. No.	Questions	CO	Skills
11.	Explain the operations of doubly linked list with examples. Write algorithms for the same.	1	U
<b>OR</b>			
12	State the polynomial representation for $6x^3+7x^2+3x+2$ using linked list. Write procedures to add and multiply 2 polynomials and explain with examples	1	U
13	Write the following routines to implement binary search tree operations: i. Perform deletion of an element in binary search tree. ii. Find_min and Find_max.	3	U
<b>OR</b>			

14	Write an algorithm for preorder, inorder and postorder traversal of a Binary tree. Explain with an example.	3	U
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Part – C (Answer all the questions 1 x 10 = 10 marks)

Q. No.	Questions	CO	Skills
15.a	Derive the expression tree for the postfix expression: (a-b) / ((c*d) + e). Write the procedure for the same. (6)	3	A
15.b	Construct a binary search tree using the following list of numbers 14,15,4,9, 7,18,3,5, 16,4,20,17, 9, 14,5. (4)	3	A
OR			
16.a	Give the various types of rotations in AVL tree during the insertion of a node? (3)	3	U
16.b	Insert the following elements step by step in sequence into an AVL tree 44,30,76,16,39,37,10,15,56,18,72 (7)	3	A

Bloom's Skills: Remember (R), Understand (U), Apply (A), Analyze (AZ), Evaluate (E) and Create (C). Skill need to be mentioned against each question

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**Saranathan College of Engineering**  
**Tiruchirapalli**

Internal Assessment Test – II			Date/ Session	20/08/2019 FN	Marks	50
Course code	IT8501	Course Title	Web Technology			
Batch No.		Duration	90 mins	Academic Year	2019-2020	
Year/	III	Semester/ Section	V / A	Department	IT	

**Part – A**

(Answer all the questions 10 x 2 = 20 marks)

Q. No.	Questions	CO	Skills
1	List the different methods defined in document and window object of java script	C304.2	U
2	Write a Java Script to print "Good Day" using If-Else condition.	C304.2	U
3	What is a Javascript statement? Give an example	C304.2	R
4	Define Host Objects	C304.2	R
5	What is a Scriptlet?	C304.2	R
6	List and explain any four HTML intrinsic event attributes.	C304.2	R
7	What is meant by DHTML?	C304.2	R
8	List the different life cycle methods of Java Servlets	C304.3	R
9	What is the purpose of cookies?	C304.3	R
10	How is session tracking achieved by URL rewriting	C304.3	R

**Part – B**

(Answer all the questions 2 x 10 = 20 marks) Maximum 2 splits/Question

Q. No.	Questions	CO	Skills
11	Explain the way in which java script handles arrays with example	C304.2	U
	OR		
12	Explain objects and arrays in java script with suitable example.	C304.2	U
13	Write a servlet to illustrate the principles of Cookies and explain with example.	C304.3	R
	OR		
14	Explain the purpose of the following DOM methods. i) getElementById ii) createElement iii) createTextNode iv) appendChild v) parentNode	C304.3	R

**Part – C**

(Answer all the questions 1 x 10 = 10 marks) Case study or Analytical type

Q. No.	Questions	CO	Skills
15	Explain in detail about dynamic content generation by a servlet with an example.	C304.3	U
	OR		
16	Write a code for creating cookies with name of person and secret code at the server, after getting these details from client using HTML form. How these details at the server side using HTTP servlet are displayed?	C304.3	A

*Bloom's Skills: Remember (R), Understand (U), Apply (A), Analyze (AZ), Evaluate (E) and Create (C). Skill need to be mentioned against each question*

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# SARANATHAN COLLEGE OF ENGINEERING

## Tiruchirappalli-620012



Add Conference	Add Journal	Add Workshop/Seminar/PSA	Add Books	Add Projects	Print Profile
Attendance	3A Test	Faculty	AISC	Study Materials	TP
Student					Exit

### Classwise Internal Assessment Analysis Report

#### Class:IT Semester-3 Section-A: Assessment Test -2

Marks/Subject	CS8351 namyar-ecg 17/08/2019	CS8391 bhavan-8 19/08/2019	CS8392 senthilraj-6 20/08/2019	EC8394 sivagamasundaram-ecg 21/08/2019	MA8351 sakthimadani-mat 18/08/2019
0 to 8	1	1	1	4	0
10 to 19	1	2	0	2	1
20 to 29	7	1	2	0	3
30 to 39	5	3	8	5	2
40 to 49	1	4	2	8	8
50 to 59	1	1	2	0	6
60 to 69	10	12	3	7	10
70 to 79	18	18	13	10	9
80 to 89	7	5	14	14	11
90 to 99	8	8	11	13	7
100 to 100	2	1	4	0	1
Presentates	61	61	61	61	60
Absentees	0	0	0	0	1
No. of Students Passed	45	47	43	44	38
No. of Failed Students	15	14	16	17	22
Total No. of Candidates	61	61	61	61	60
Pass Percentage	74	77	74	72	62
Average	54.85	58.07	62.82	56.57	56.83
Class Pass Percentage -> Total Students:61, Total Passed:28, Total Failed:33 Pass % = 46					

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# SARANATHAN COLLEGE OF ENGINEERING

## Tiruchirappalli-620012



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 Attendance | SA Test | Faculty | AUC | Study Materials | TP | Exit

### ClassWise Internal Assessment Analysis Report


#### Class:IT Semester:5 Section:A: Assessment Test -2

Mark/Subject	CS8494 radonprap-2 19/08/2019	CS8581 sumathi-2 17/08/2019	EC6691 vishanari-ec 21/08/2019	IT7503 mempai-2 20/08/2019	MA8553 mnanthi-mat 16/08/2019	OCES52 mnanthi-2 22/08/2019
0 to 9	1	1	4	0	5	1
10 to 19	2	2	1	1	3	0
20 to 29	0	2	3	0	5	2
30 to 39	1	6	1	2	8	5
40 to 49	3	0	2	1	2	3
50 to 59	2	1	2	2	3	0
60 to 69	5	17	4	10	4	5
70 to 79	8	19	12	17	12	19
80 to 89	14	4	16	18	9	14
90 to 99	13	4	9	5	2	5
100 to 109	8	0	3	0	1	0
Presenters	55	51	55	55	54	54
Examiners	0	4	0	0	1	1
No. of Students Passed	48	29	44	50	28	42
No. of Failed Students	7	12	11	5	26	11
Total No. of Candidates	55	55	55	55	55	55
Pass Percentage	87	71	80	91	51	78
Average	69.42	55.06	62.47	64.04	47.09	60.44
Class Pass Percentage -> Total Students:55 , Total Passed:24, Total Failed:31, Pass % = 44						

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# SARANATHAN COLLEGE OF ENGINEERING

## Tiruchirappalli-620012



Add Conference
Add Journal
Add Workshop/Seminar/POA
Add Books
Add Projects
Print Profile

Attendance
SA Test
Faculty
ALOC
Study Materials
TP
Exit

## Classmate Internal Assessment Analysis Report

Class: IT Semester: 7 Section: A:  
Assessment Test - 2

Mark/Subject	CS67021 Intro to AI 15/08/2019	CS67023 wasm/jsumar-a 15/08/2019	IT16004 padmapriya-a 20/08/2019	IT16701 anand-a 21/08/2019	IT16702 shreyasi-a 17/08/2019
0 to 9	1	0	0	0	0
10 to 19	2	2	0	0	2
20 to 29	2	2	1	2	2
30 to 39	5	2	2	1	4
40 to 49	2	1	1	1	2
50 to 59	2	1	1	2	2
60 to 69	4	5	1	9	13
70 to 79	20	15	10	12	21
80 to 89	14	11	6	18	14
90 to 99	7	14	20	12	0
100 to 100	0	1	14	0	0
Presentences	58	56	57	57	58
Absentences	0	3	2	2	1
No. of Students Passed	43	46	52	51	48
No. of Failed Students	14	10	5	6	10
Total No. of Candidates	57	56	57	57	58
Pass Percentage	75	78	88	86	81
Average	59.73	63.89	76.38	66.46	57.28

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## SARANATHAN COLLEGE OF ENGINEERING

28.08.2019

### STAFF CIRCULAR

Internal Assessment Test- I for 1 Year B.E./B.Tech (A to K Sections) will commence from 04.09.2019 (Wednesday) as per the timetable displayed in the notice board.

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**Test duration:**

**9.30 a.m. to 11.00 a.m. (1 ½ Hours)**

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#### Class timings after the I.A. Test

**11.00 a.m. - 11.15 a.m. : Break**

**11.15 a.m. - 12.00 noon. : 3rd Hour (Duration: 45 min)**

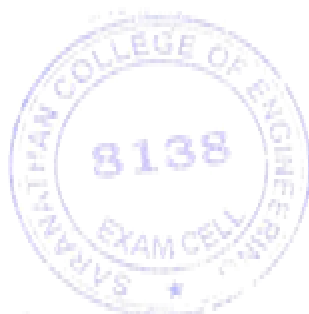
**12.00 noon. - 12.45 p.m. : 4th Hour (Duration: 45 min)**

Afternoon classes will be held as per regular timetable.

Staff members handling subjects for the above classes are requested to set the question paper for each subject ( In the attached format). **The question papers for all subjects may be sent on or before 31.08.2019 (Saturday) 11.00 a.m. to [examcell@saranathan.ac.in](mailto:examcell@saranathan.ac.in)**

**Note:** 1. The question papers should be sent in the attached format paper [Portrait]  
(1 question paper per page).

2. Common Question Paper should be followed for Common Subjects of various Branches.



  
**PRINCIPAL**



**SARANATHAN COLLEGE OF ENGINEERING**  
**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**

TIME TABLE FOR INTERNAL ASSESSMENT TEST - I FOR II (III SEM), III (V SEM), IV (VI SEM) YEAR B.E., CSE ODD SEM 2019  
 Time : 9.30 a.m. to 11.00 a.m.

DATES	II CSE - A & B	III CSE - A & B	IV CSE - A & B
17.07.19	MAB351 MATHEMATICS Ms.V.S.AKILANDESWARI SEC Ms.N.SUBHASHINI	MAB551 Algebra and Number Theory A-SEC Dr.R.NEELAMBARI B-SEC Dr.S.SIVAMANI	CS 6701 CRYPTOGRAPHY & NETWORK SECURITY A-SEC Ms.R.THILLAIKKARASI B-SEC Ms.V.PUNITHA
18.07.19	CS8351 DIGITAL PRINCIPLES & SYSTEM DESIGN A-SEC Mr. Mr.A.T.BARANI VIJAYAKUMAR B-SEC Ms.K.MOHANAPRIYA	CS8591 Computer Networks A-SEC Mr.S.VENKATA SUBRAMANIAN B-SEC Mr.P. DINESH KUMAR	CS 6702 GRAPH THEORY & APPLICATIONS A-SEC Dr.S.ANUPRIYA B-SEC Dr.S.ARUNKUMAR
19.07.19	CS8391 DATA STRUCTURES A-SEC Ms.V.PUNITHA B-SEC Dr.P.I. RAJARAJESWARI	CS8501 THEORY OF COMPUTATION A-SEC Ms.R.SENTHAMIL SELVI SEC Ms.R.THILLAIKKARASI	CS 6703 GRID AND CLOUD COMPUTING A-SEC Mr.A.T.BARANI VIJAYAKUMAR B-SEC Ms.R.SENTHAMIL SELVI
20.07.19	CS8392 OBJECT ORIENTED PROGRAMMING A-SEC Dr.S.MOHANA B-SEC Mr.K.S.CHANDRASEKARAN	CS8592 OBJECT ORIENTED ANALYSIS & DESIGN A-SEC Ms. N.SAI PRABA B-SEC Dr.S.A.SAGAAYA ARJUL MARY	CS 6704 RESOURCE MANAGEMENT TECHNIQUES A-SEC Ms.V.S.AKILANDESWARI B-SEC Ms.R.PADMA
22.07.19	EC8395 COMMUNICATION ENGINEERING A-SEC Mr.SRIHARAN BALAJI B-SEC Mr.V.KOUSHICK	EC8691 Microprocessors and Microcontrollers A-SEC Ms.M.ANTHUVAN LYDIA B-SEC Ms.V.RAMYA	CS 6007 INFORMATION RETRIEVAL A-SEC Ms.R.KEERTHANA B-SEC Mr.M.ANBABHAGAN
23.07.19		OEC552 SOFT COMPUTING A-SEC Mr.T.SATHISHKUMAR B-SEC Ms.N. KAVITHA	IT 6801 SERVICE ORIENTED ARCHITECTURE A-SEC Mr.C.BOOBALA MURALITHARAN B-SEC Mr. K.S.CHANDRASEKARAN

*[Signature]*  
 [EXAM COORDINATOR] 12/7/19

*[Signature]*  
 [HOD] 12/7/19

SARANATHAN COLLEGE OF ENGINEERING.				
INVIGILATION DUTY FOR I.A.TEST- 1 EVEN SEM - 2020				
S.No.	NAME OF THE STAFF	6.1.2020	7.1.2020	8.1.2020
1	Ms. R Senthamil Selvi			
2	Mr. T Sathis Kumar			
3	Ms. N Kavitha		*****	
4	Mr. M Anbazhagan	*****	*****	
5	Ms. K Mohanappriya	*****		
6	Ms. J Sathiaparkavi		*****	*****
7	Mr. A.T Barani Vijaya Kumar	*****		
8	Mr. R Mohankumar	*****		*****
9	Mr. P Dineshkumar			
10	Mr.P.B. Arunprasad	*****		
11	Mr. R Karthik	*****		*****
12	Ms. R Sharanya	*****		*****
13	Mr.S.Hariprasath	*****		*****
14	Mr.K.Malaisamy			
15	Ms.P.Sivagamasundari			
16	Mr.J. Eindhumathy	*****		*****
17	Ms.A. Shamim Banu			
18	Mr.M.Mahendran			*****
19	Mr.G.Sivakannu			*****
20	Ms.M.Anthuvan Lydia		*****	
21	Mr.V.Koushick		*****	*****

22	Ms.G.Lakshmi		*****	
23	Ms.R.Devi			*****
24	Ms.R.Vaishnavi	*****		*****
25	Mr.V. Dinesh			*****
26	Mr.SD.Sairam			*****
27	Dr. K. Rajkumar			*****
28	Mr.M.Marimuthu			
29	Ms.C.Pearline Kamalini	*****		*****
30	Mr.S.Ramprasath			
31	Mr.P.K. Arun Kumar			
32	Mr.P. Ram Prakash	*****		
33	Mr.P.Ramesh Babu		*****	
34	Mr.R. Sridhar			
35	Mr.R.Venugopal	*****		
36	Mr.R.Satheesh		*****	
37	Mr.R.Vijay		*****	
38	Ms.N.Gayathri	*****		*****
39	Mr.S.Sivakumar		*****	
40	Ms.A.R. Danila Shirly	*****		*****
41	Mr. P. Thirumurugan			
42	Mr.P. Aravind		*****	
43	Mr. S Vigneshwaran		*****	
44	Mr. R. Seetharaman		*****	
45	Ms.Ezhilarasi			
46	Ms.J.Sangeethapriya	*****		
47	Mr.P. Anand	*****		
48	Mr.R. Rengaraj	*****		



49	Ms.A. Sheelavathy	*****	*****	*****
50	Ms.M. Padma Priya		*****	*****
51	Ms.C. Nivethitha		*****	
52	Mr.A. Anandraj		*****	*****
53	Ms.A. Sagaya Bastina		*****	
54	Ms .P. Vaishali		*****	
55	Mr.S. Kannan			
56	Mr.R.Suresh Babu			
57	Dr.G..Mahesh			
58	Mr.S. Sathyanarayanan			
59	Dr.A. Mercy Vasan			
60	Mr.S. Karthikeyan			
61	Mr.A. Saravanan			
62	Mr.R. Kumar			
63	Mr.M. Sridharan			
64	Mr.A. Ranjith Raj			
65	Mr.M. Sriram Prasanth			
66	Mr.Kattera Sai Krishna			
67	Mr.J. Sivasubramanian		*****	
68	Mr.P.V. Rajesh		*****	
69	Ms.R.S. Shiva Ranjani			
70	Mr.S. Vinoth Kumar			
71	Mr.P. Vigneshwar			
72	Mr.S. Paramaguru	*****		
73	Mr.K. Amarnath			
74	Dr. D Geetha,			
75	Dr. S Sivamani			

76	Dr. S Revathi,			*****
77	Dr. S Arunkumar,			
78	Dr. P.K. Lakshmi Devi			
79	Dr. S Anupriya,		*****	
80	Ms. R Padma,			
81	Ms. N Subashini,	*****		
82	Dr.V. Balamurugan			*****
83	Mr.B. Balaprasad			

21

21

21

V. Lakshmi

SARASWATHI COLLEGE OF  
ENGINEERING

SQUARD			
			TEST COORDINATORS
			Mr. D Boobala Muralitharan
Mr. K.S Chandrasekaran			Ms.V.Ramya
Dr.M.Padma	6.1.2020		Mr.N.Vijayasarithi
Dr.S.A.Arjunmozhi			Mr.V. Manoj Kumar
Ms.R. Thillaikarasi	7.1.2020		Mr.T. Tamilarasan
Dr.V.Mohan			Mr.G. Venkaesan
Dr.P.Shannugapriya	8.1.2020		Dr.M. Ganesan
Dr.M.Baritha begum			
Dr.S.Vijayalakshmi	9.1.2020		
Mr.B.Paranathagan			
Mr.R.Balasubramanian	10.1.2020		
Dr.M.Shannugavalli			
Dr.R.Rekha	11.1.2020		



SARANATHAN COLLEGE OF  
ENGINEERING

V. Venkatesh



Academic Year: 2019-20

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## SARANATHAN COLLEGE OF ENGINEERING

26.08.2019

### STAFF CIRCULAR

Retest – II for Internal Assessment Test- II for II, III & IV B.E / B.Tech classes will commence from 27.08.2019 (Tuesday) as per the timetable displayed in the Notice Board.

RE-TEST FEE of Rs 50/- per Subject will be collected only from those students who have secured less than 50% or absent in I.A Test- II. However the students who have secured between 50% and 60% in I.A Test- II must attend the Re-Test II along with failures and absentees without fail.

Note: The absentees with genuine reason and with proper permission can be exempted from the RE-TEST FEE.

The minimum pass mark for Retest will be 60 out of 100.

Test duration: 5.00 p.m. to 6.30 p.m. (1 1/2 Hrs)

Max Marks : 50



  
PRINCIPAL

PRINCIPAL  
Saranathan College of Engineering  
TRICHY - 12.

## **PRACTICAL COURSES**

SARANATHAN COLLEGE OF ENGINEERING  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
MODEL LAB PRACTICAL EXAMINATION – NOV2018

Fifth Semester

**B.E Electronics and Communication Engineering**  
**EC6513 - Microprocessors and Microcontrollers Lab**

**Duration : 3 hours**

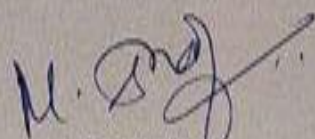
**Max.Marks :100**

1. (a) Write a DAC interface program for generating Square Waveform (50)  
(b) Write an ALP to perform 16-bit data addition using 8086 mnemonics. (50)
2. (a) Write an ALP to Sort N elements in ascending order in an array using 8086 mnemonics. (50)  
(b) Write an ALP to perform 16-bit data subtraction using 8086 mnemonics. (50)
3. Interface 8279 with 8086 and write an ALP to display the character "HELP" and to read a key from keyboard. (100)
4. (a) Write an ALP to copy a string from one memory location to another using 8086 mnemonics. (50)  
(b) Write an ALP to sort the elements in an array in descending order using 8086 mnemonics. (50)
5. (a) Write an ALP using 8051 mnemonics to make the stepper motor to rotate with a specific delay by continuously energizing the coils. Stepper Motor port address – FFC0 (50)  
(b) Write an ALP to perform 16-bit data multiplication using 8086 mnemonics. (50)
6. (a) Write an ALP to perform 32-bit data division using 8086 mnemonics. (50)  
(b) Write an ALP to generate a square wave of time period through P1.2 of 8051. (50)
7. Write an ALP to communicate serially between two 8051 microcontrollers. (100)
8. (a) Interface a 8 bit ADC with 8086 and write an ALP to convert analog values into corresponding digital data's. Verify theoretically. (50)  
(b) Write an ALP to find the smallest element in an array using 8086 mnemonics. (50)
10. (a) Interface a 8 bit DAC with 8086 and write an ALP to generate a sawtooth wave. (50)  
(b) Write an ALP to find the largest element in an array using 8086 mnemonics. (50)



11. (a) Interface a 8 bit DAC with 8086 and write an ALP to generate a triangular wave. (50)
- (b) Write an ALP to perform basic logical operation using 8086. (50)
12. (a) Write an assembly language program to find 2's complement of a number using 8051 (50)
- (b) Write an ALP to perform basic logical operation using 8051. (50)
13. (a) Write an ALP to find the length of a string using 8086 mnemonics. (50)
- (b) Write an ALP to perform 8-bit data addition using 8051 mnemonics. (50)
14. (a) Write an assembly language program to find the square of a number using 8051 (50)
- (b) Write an ALP to fill five memory locations with the given data using 8086 mnemonics. (50)
15. (a) Write an ALP to perform 8-bit data multiplication using microcontroller 8051 mnemonics. (50)
- (b) Write an assembly language program to find the cube of a number using 8051 (50)
16. (a) Write the parallel communication interface program to send and receive data. (100)
17. (a) Write an assembly language program to find 2's complement of a number using 8051. (50)
- (b) Write an assembly language program to display the system date using 8086 (50)
18. Write an assembly language program to study interfacing technique of UART with Microprocessor 8086 and write an 8086 ALP to transmit and receive data between two serial ports with RS 232 cable. (100)
19. (a) Write an ALP to perform basic logical operation using MASM. (50)
- (b) Write an ALP for digital clock, printer status using 8086 (50)
20. (a) Write an assembly language program to interface traffic light controller. (100)
21. (a) Write an assembly language program to perform BCD addition & subtraction using 8086 (50)
- (b) Write an ALP to find the largest element in an array using 8086 mnemonics. (50)
22. Write 8086 ALP to rotate a stepper motor for given number of steps at a given angle and in the given direction of rotation based on the user choice. (100)

  
Staff Signature

  
HOD/ECE



1. Plot the radiation characteristics of a loop antenna using a suitable simulation tool. Also measure its directivity, gain and 3 dB beamwidth.
2. With a suitable simulation tool, construct an experimental set up to plot the radiation characteristics of half wave dipole antenna. Also measure its directivity, gain and 3 dB beamwidth.
3. Using stochastic computer simulation, analyze the performance of PSK and DPSK modulation systems in AWGN channel by calculating Bit Error Rates.
4. Analyze the BER performance of Direct Sequence spread spectrum CDMA system using a suitable simulation tool.
5. Using stochastic computer simulation, analyze the performance of QPSK and BPSK modulation systems in AWGN channel by calculating Bit Error Rates.
6. Using a suitable simulation tool, execute the Least Mean Square weight adaptation algorithm for updating the coefficients of FIR Adaptive filter. Analyze its performance with different values of step size.
7. Analyze the BER Performance of OFDM transceiver with a suitable digital modulation scheme using simulation
8. Construct the microwave test bench for measuring S parameters of the following microwave N – port devices;
  - i) Circulator
  - ii) Isolator
9. Design a three section coupled lines coupler with a Coupling of 20 dB, center frequency 1 GHz and characteristic impedance of 50  $\Omega$ , substrate FR-4 with  $B_r = 4.2$  in a stripline with a ground plane. Find the width, separation and the length of each section. Determine the coupling factor and directivity of the coupler.
10. A. Construct the microwave test bench for measuring directivity and coupling factor of a directional coupler  
B. Verify experimentally how an isolator can be used to block reflections from reaching the microwave source in a microwave test bench
11. Plot the radiation characteristics of a microstrip patch antenna using a suitable simulation tool. Also measure its directivity, gain and 3 dB beamwidth.



12. Plot the radiation characteristics of a frequency dependent antenna using a suitable simulation tool. Also measure its directivity, gain and 3 dB beamwidth.
13. Using a suitable simulation tool, execute the Recursive Least Square weight adaptation algorithm for updating the coefficients of FIR Adaptive filter. Analyze its performance with different values of step size.
14. Measure the reflection co-efficient and VSWR of a two wire transmission line terminated with the following 3 standard load impedances.
  - i) Open circuit load
  - ii) Short circuit load
  - iii) Matched termination

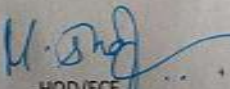
Staff Signature

HOD/ECE



SARANATHAN COLLEGE OF ENGINEERING  
DEPARTMENT OF ELECTRONICS AND COMMUNICATION  
MODEL PRACTICAL LABORATORY EXAMINATION SCHEDULE  
ACADEMIC YEAR: 2018 -2019 ODD SEMESTER

S.NO	YEAR	SEM	SUBJECT CODE	SUBJECT TITLE	DATE	STAFF INCHARGE
1	III	III	EC8361	ANALOG AND DIGITAL CIRCUITS LAB	09/10/2018, 10/10/2018	Dr. V. MOHAN, ASSO PROF/ECE Dr. M. BARITHABEGUM
2	II	III	EC8381	FUNDAMENTALS OF DATA STRUCTURES IN C LABORATORY	08/10/2018, 11/10/2018	Ms.K. MOHANAPPRIYA, AP/ECE., Ms. SUGANYA, AP/ECE
3	II	III	HS8381	INTERPERSONAL SKILLS/LISTENING AND SPEAKING	10/11/2018	Mr. Narayanan, AP/ENGLISH
4	III	V	EC6511	DIGITAL SIGNAL PROCESSING LAB	8/10/2018, 9/10/2018	Dr. P. SHANMUGAPRIYA, AP/ECE MR. G. SIVAKANNU, AP/ECE
5	III	V	EC6512	COMMUNICATION SYSTEM LAB	10/10/2018, 11/10/2018	Dr.S.RAJESWARI, ASSO/ECE Mr.S. HARIPRASATH, AP/ECE
6	III	V	EC6513	MICROPROCESSOR AND MICROCONTROLLER LAB	10/10/2018, 11/10/2018	Ms. M. ANTHUN LYDIA, P/ECE., Mr. V. Dinesh, AP/ECE
7	IV	VII	EC6711	EMBEDDED LABORATORY	10/10/2018, 11/10/2018	Ms. P. SIVAGAMASUNDHARI, AP/ECE Ms. R. DEVI, AP/ECE
8	IV	VII	EC6712	OPTICAL AND MICROWAVE LAB	10/11/2018	Dr. S. A. ARUNMOZHI, ASSO/ECE Mr.S.D.SAIRAM, AP/ECE

  
HOD/ECE

**SARANATHAN COLLEGE OF ENGINEERING, TIRUCHIRAPPALLI - 620 012**  
**DEPARTMENT OF ECE**  
**AUC WEBPORTAL ENTRY PERIOD - IV (21.06.2019 to 19.10.2019) LABORATORY SHEET**  
**III ECE B - V SEMESTER - ACADEMIC YEAR 2019 - 2020**

S No	B No	Reg No	Name	EC8561 - CS Lab		EC8562 - DSP Lab		EC8563 - CN Lab	
				Attn (60)	Mark	Attn (60)	Mark	Attn (60)	Mark
1	202117	813817106031	JOE ANTANIE. A M	60	100	57	100	57	100
2	202062	813817106060	PRASANTH. R	60	90	60	100	60	100
3	202063	813817106061	PREETHI. M	57	100	60	100	57	100
4	202064	813817106062	PREMKUMAR. S	54	95	57	100	57	95
5	202065	813817106063	PRINCY. P	57	100	57	100	57	100
6	202066	813817106064	PRIYADHARSHINI. K	60	90	60	100	60	90
7	202067	813817106065	PRIYADHARSHINI. T	57	100	60	100	57	95
8	202068	813817106066	RAGHUNATH. K .S	60	95	57	95	60	90
9	202069	813817106067	RAHUL. B	57	90	57	95	60	90
10	202070	813817106068	RAJALAKSHMI. K	57	95	54	100	60	100
11	202071	813817106069	RAKAVI. R	57	95	57	95	57	95
12	202073	813817106071	RASHEED RAJA. G	57	90	51	95	57	90
13	202074	813817106072	REASHMA. N	54	100	57	95	60	100
14	202075	813817106073	REHANA BEGUM. A	57	95	60	100	60	100
15	202076	813817106074	RENITA BLESSY. B	60	95	60	95	60	100
16	202078	813817106076	ROSHINI. R	57	95	60	95	60	100
17	202079	813817106077	SAKTHI. T	57	100	60	100	60	100
18	202080	813817106078	SANTHOSHWARAN. G	60	100	57	100	57	100
19	202081	813817106079	SARANYA. M	60	100	60	100	57	100
20	202082	813817106080	SATHIYAKRISHNAN. P	60	90	57	95	54	90
21	202083	813817106081	SHAHIN SULTHANA. M .W	57	100	57	100	60	100
22	202084	813817106082	SHAIK AYISHA KAREEMA.	54	100	60	100	54	100
23	202086	813817106084	SHARON FRANCIS. A	57	100	54	100	60	100
24	202087	813817106085	SHARON ROSE ROOBAVATHY. S	54	90	57	95	54	95
25	202088	813817106086	SHARUMATHY. S. B	60	100	57	100	60	100
26	202089	813817106087	SIVASANKAR. N	54	95	51	95	54	85
27	202090	813817106088	SIVASRI. M	57	100	60	100	57	100
28	202091	813817106089	SOWMIYA. K	57	100	57	100	57	100
29	202092	813817106090	SRI JAYA ROHINI. J	60	95	60	100	60	100

S No	B No	Reg No	Name	EC8561 - CS Lab		EC8562 - DSP Lab		EC8563 - CN Lab	
				Attn (60)	Mark	Attn (60)	Mark	Attn (60)	Mark
30	202093	813817106091	SRI SWARNA. G	54	95	54	100	54	100
31	202094	813817106092	SUBHASHINI. S	57	100	57	100	60	100
32	202095	813817106093	SUBRAMANI. G	57	90	57	95	54	90
33	202096	813817106094	SUJITHA. B	57	95	54	95	60	95
34	202097	813817106095	SUJITHRA. R	57	100	60	95	57	95
35	202098	813817106096	SWATHI. K	57	95	57	95	54	100
36	202100	813817106097	SWATHI. S	60	100	57	100	60	100
37	202101	813817106098	SYED HAKKIM. M	54	90	57	100	57	90
38	202102	813817106099	TAMIZH MALAR A.K	57	100	60	100	60	100
39	202103	813817106100	TENA TIFANY EDITH. I	54	90	57	95	54	90
40	202104	813817106101	THIRISA SUCI	57	95	57	95	54	90
41	202105	813817106102	VAISHALI. T	57	100	57	100	60	100
42	202106	813817106103	VAISHNAVI. T	54	100	60	95	60	100
43	202107	813817106104	VANITHA. K	60	100	60	100	60	100
44	202108	813817106105	VARSHA DHANASHRI. S	57	100	57	100	60	100
45	202109	813817106106	VARUN BHAASKAR	54	100	57	100	54	100
46	202110	813817106107	VEDIKA LAKSHMI. K	60	100	54	100	60	100
47	202111	813817106108	VEERARAGAVAN. N	54	95	57	95	54	90
48	202112	813817106109	VINOTHINI. N	57	95	57	95	57	100
49	202113	813817106110	VISWANATHAN C	54	95	54	95	57	90
50	202114	813817106111	YALINI. E	60	90	54	95	57	95
51	202115	813817106112	YASODHA. N	60	90	57	95	57	90
52	202116	813817106113	YELLAMRAJU VENKATA NAGA ANJANI JYOTHI.	60	100	57	100	54	100
53	202122	813817106301	AISWARYA. D	51	90	51	0	51	85
54	202120	813817106303	KAVUSHIKA. B	57	90	57	95	60	100
55	202119	813817106305	PADMA SHRUTHI. S	60	100	60	100	60	100
56	202125	813817106701	ZIAD AHMED. M	54	95	54	95	54	90
57	202124	813817106702	NISHANTHINI. B	60	90	54	95	60	100

Signature of the Faculty

Class Co-ordinator(s)

Faculty Advisor

HOD / ECE

Principal



Ex.No: 6

# DESIGN AND IMPLEMENTATION OF ADDER AND SUBTRACTOR USING LOGIC GATES.

DATE: 02.02.19

Aim:-

To design and implement the following Combinational circuits using logic gates.

- A) Half adder.
- B) Half subtractor.
- C) Full Adder and
- D) Full subtractor.

COMPONENTS REQUIRED:-

S.No	Apparatus	Quantity
1.	Digital trainer kit	1
2.	IC 7432, IC 7408, IC 7404, IC 7486	1 each
3.	Patch cords	As required
4.	Connecting wires	as required

PROCEDURE:-

- 1) Give connections as per the logic diagram.
- 2) Give inputs as per truth table, For logic 1, connect the input pin to +5V and for logic zero; Connect the input pin to ground.
- 3) Verify the corresponding truth table.



HALF ADDER:-TRUTH TABLE:-

A	B	CARRY	SUM
0	0	0	0
0	1	0	1
1	0	0	1
1	1	1	0

K-Map for SUM:-

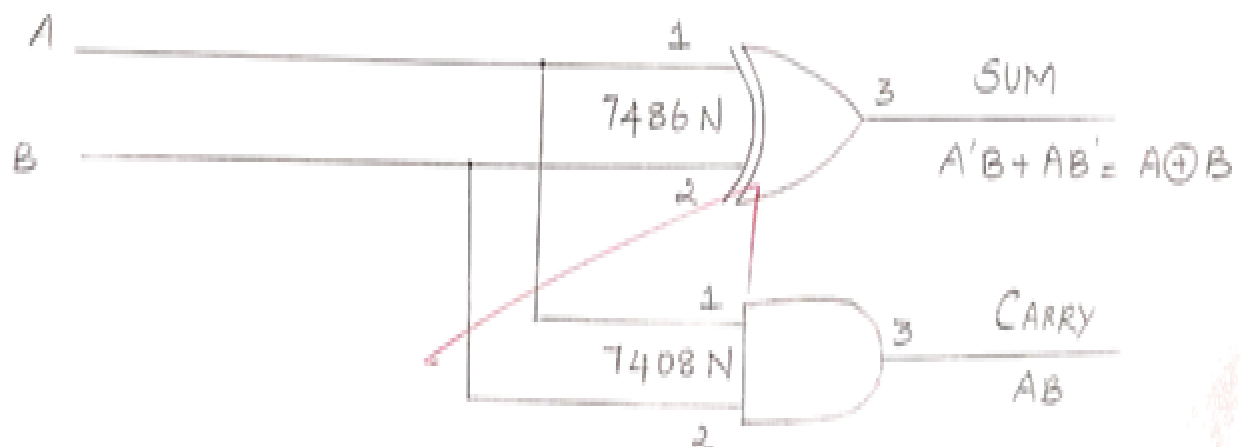
A \ B	0	1
0		1
1	1	

$$\begin{aligned} \text{SUM} &= A'B + AB' \\ &= A \oplus B \end{aligned}$$

K-Map for CARRY:-

A \ B	0	1
0		
1		1

$$\text{CARRY} = AB$$

LOGIC DIAGRAM:-

FULL ADDER:-

TRUTH TABLE:-

A	B	C	CARRY	SUM
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

K-Map for SUM:-

A \ BC	00	01	11	10
0		1		1
1	1		1	

$$SUM = A'B'C + A'BC' + AB'C' + ABC \Rightarrow A'(B'C + Bc') + A(B'C' + Bc)$$

$$= A \oplus B \oplus C$$

K-Map for CARRY:-

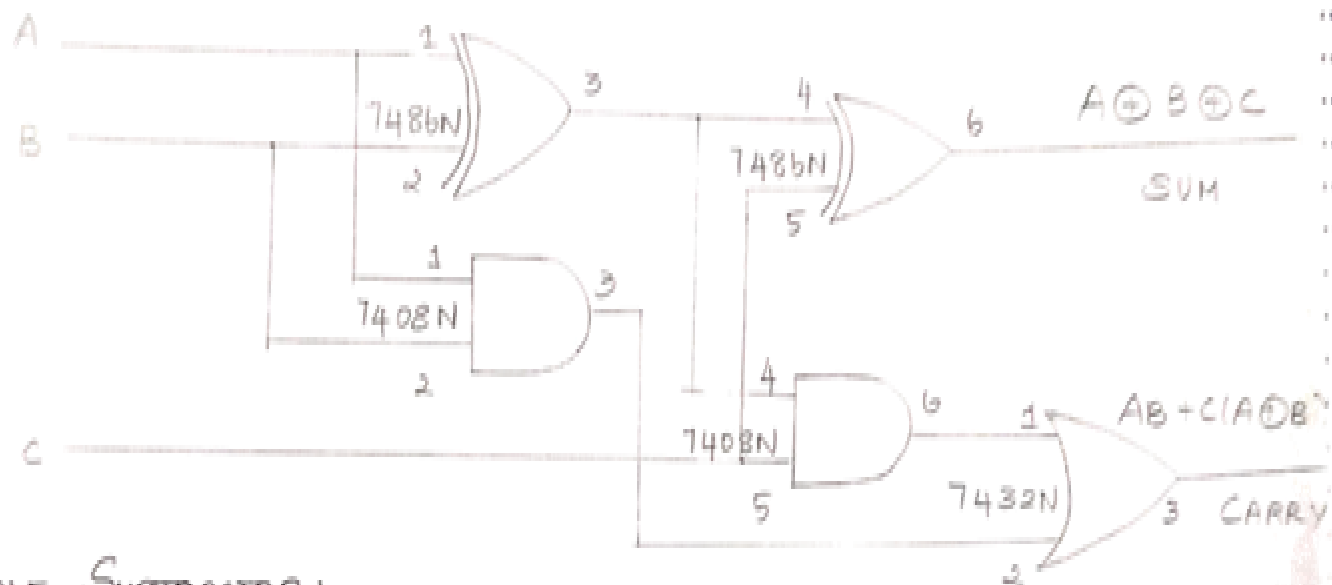
A \ BC	00	01	11	10
0			1	
1		1	1	1

$$\text{CARRY} = AB + BC + CA$$

$$= A'BC + AB'C + ABC + ABC' = AB(C+C') + C(A'B + AB')$$

$$= AB + C(A \oplus B)$$

LOGIC DIAGRAM:-



HALF SUBTRACTOR:-

TRUTH TABLE:-

A	B	Borrow	Difference
0	0	0	0
0	1	1	1
1	0	0	1
1	1	0	0

K-Map for DIFFERENCE:-

A \ B	0	1
0		1
1	1	

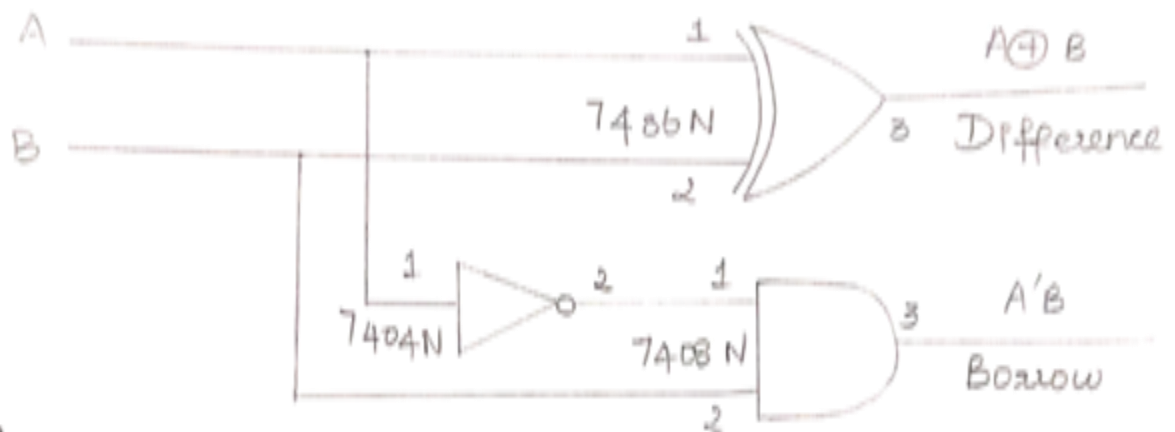
K-Map for BORROW:-

A \ B	0	1
0		1
1		

$$\begin{aligned}\text{Difference} &= A'B + AB' \\ &= A \oplus B\end{aligned}$$

$$\text{Borrow} = A'B$$

LOGIC DIAGRAM:-



FULL SUBTRACTOR:-

TRUTH TABLE:-

A	B	C	Borrow	Difference
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	1	0
1	0	0	0	1
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1

K-Map for Difference:-

A \ BC	00	01	11	10
0		1		1
1	1		1	



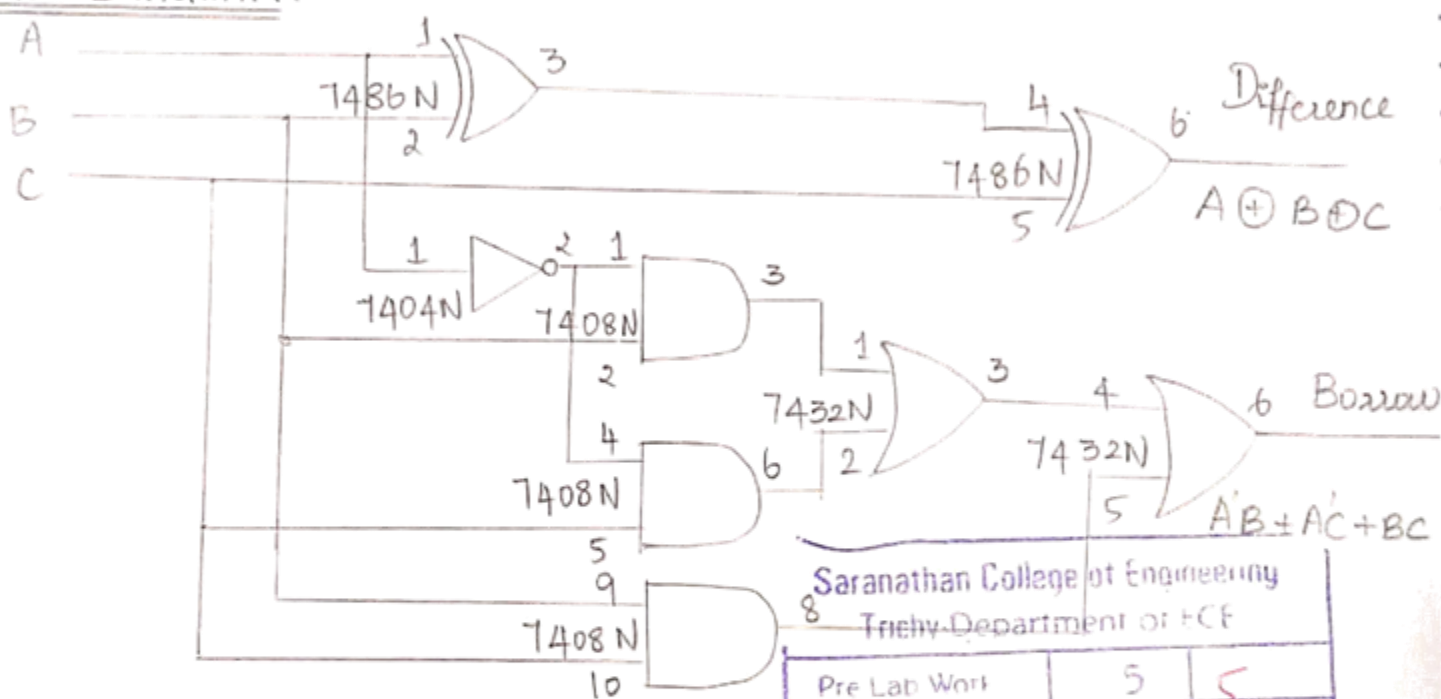
$$\begin{aligned} \text{Difference} &= A'B'C + A'BC' + AB'C' + ABC = A'(B'C + BC') + A(B'C' + BC) \\ &= A'(B \oplus C) + A(B \oplus C)' = A \oplus B \oplus C \end{aligned}$$

K-Map for Borrow:-

A \ BC	00	01	11	10
0		1	1	1
1			1	

$$\text{Borrow} = A'B + BC + A'C$$

LOGIC DIAGRAM:-



RESULT:-

*gmm* 05/2/19

Saranathan College of Engineering Tiruchy Department of ECE		
Pre Lab Work	5	5
Conduction	5	5
Observation	5	5
Viva Voce	5	4
Total	20	19

Thus, the half adder, half subtractor, full adder and full subtractor of combinational circuit using logic gates are designed and implemented and has been verified.

# CONTENTS

Exercise No.	Date	Name of the Exercise	Page No.	Remarks
1.	29.12.18	Inverting and non-inverting amplifier	1	Good 11/1
2.	07.01.19	Adder and comparator	11	Good 11/1
3.	12.01.19	Integrator and differentiator	17	Good 11/1
4.	12.01.19	Astable and Monostable multivibrator using NE555	27	Satisfactory 11/1
5.	28.01.19	Dc power supply using LM317	37	Satisfactory 11/1
6.	02.02.19	Study of Basic gates	43	Good 11/2
7.	02.02.19	Design of and implementation of adder and subtractor using logic gates.	53	Good 11/2
8.	08.02.19	Design and Implementation of code converters.	65	Satisfactory 11/2
9.	19.02.19	Design and Implementation of Encoder and decoder.	89	Satisfactory 11/2
10.	25.02.19	Design and Implementation of MUX and DEMUX	101	Good 11/3
11.	25.02.19	Implementation of Boolean function.	109	Good 11/3
12.	25.02.19	Implementation of odd/even parity checker/generator.	117	Good 11/3

## CONTENTS

Exercise No.	Date	Name of the Exercise	Page No.	Remarks
13.	02/03/19	Design and Implementation of counters	129	Satisfied
14.	12/03/19	Implementation of shift registers.	139	Satisfied
15.	13/03/19	Voltage controlled oscillator	149	Approved
16.	13/03/19	Frequency multipliers using PLL (Value Added Expt.)	157	Approved

Completed

Approved  
15/03/2019



EXNO: 16

**VALUE ADDED EXPERIMENT**  
**FREQUENCY MULTIPLIER USING PLL**

DATE: 13.03.19

AIM:

To construct a frequency multiplier circuit using PLL.

APPARATUS REQUIRED:

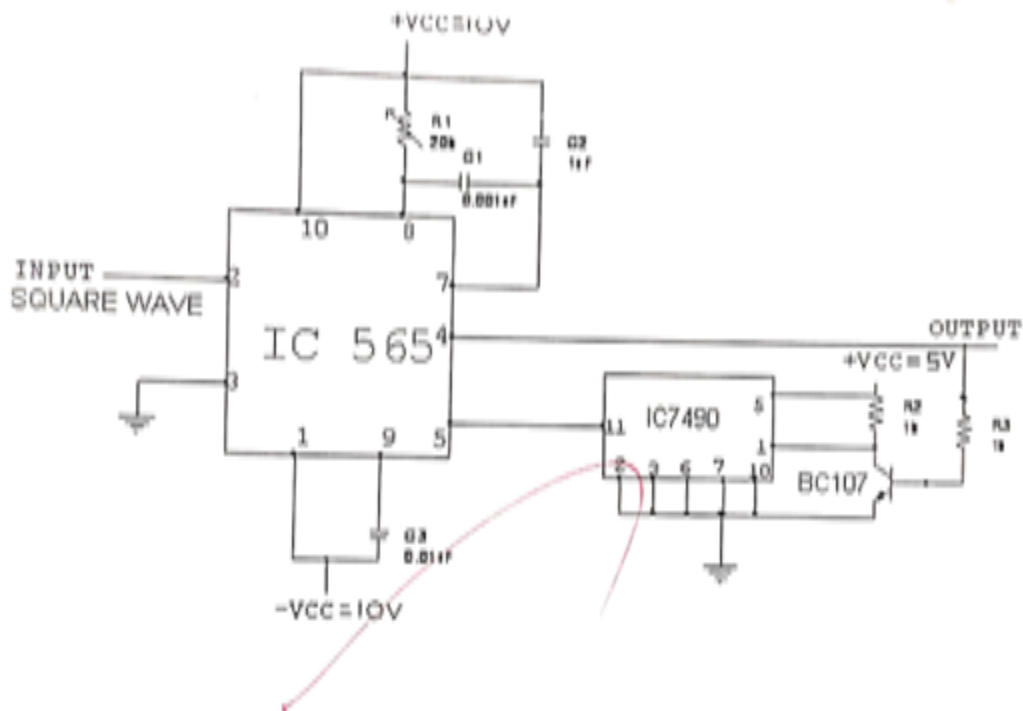
S.No	Apparatus	Range	Quantity
1.	Resistor	2,10,4,7 k $\Omega$	1
2.	Potentiometer	20k	1
3.	Capacitor	1,10nF,1pF	1,1,1
4.	4-Bit Binary Counter	IC7490	1
5.	PLL IC	IC565	1
6.	Transistor	BC107	1
7.	Dual RPS	$\pm 15$ v	-
8.	AFO		
9.	Bread board		
10.	Connecting wires		

**PROCEDURE:**

1. Connections are made as per the circuit diagram.
2. Set the input signal at 1 volt square wave at 500 Hz.
3. Vary the VCO frequency by adjusting the 20k $\Omega$  potentiometer till the PLL is locked



## FREQUENCY MULTIPLIER USING PLL



S.No	Waveform	Amplitude(v)	Time (ms)	Frequency(Hz)
1.	Input (Square)	2V	0.48 $T_{ON}$ 0.52 $T_{OFF}$	1 kHz
2.	Output(Square)	2V	0.096 $T_{ON}$ 0.104 $T_{OFF}$	5 kHz

**RESULT:**

Thus, the Frequency Multiplier circuit using PLL IC 565 has been designed, implemented and verified.

**Saranathan College of Engineering**  
Trichy-Department of ECE

Pre Lab Work	5	5
Conduction	5	5
Observation	5	5
Viva Voce	5	4
Total	20	19

## SEMINAR / CASE STUDY

SARANATHAN COLLEGE OF ENGINEERING, TRICHY-12

DEPARTMENT OF ECE

EC 8611 -Technical Seminar

III ECE A

S.No	Batch No.	Reg.No	Name	Title	Date of Presentation	Seminar Paper(40)	Presentation (40)	Querries(20)	Total (100)
1	192116	813817106501	MAHALAKSHMI A	Flexible OLED	16.12.2019	38	38	18	94
2	202002	813817106001	AISHWARIYA DHEVI. G	Blue Eye technology	16.12.2019	39	39	18	96
3	202003	813817106002	AKILA. S	Virtual Reality	16.12.2019	38	38	18	94
4	202004	813817106003	ALAGAMMAI. K	Smart note taker	16.12.2019	39	39	18	96
5	202005	813817106004	ANTO SAGAYA SHIRLEY R	Holographic Display	16.12.2019	40	39	17	96
6	202006	813817106005	ASHOK KUMAR. V	DAB technology	16.12.2019	35	38	17	90
7	202007	813817106006	ASIK BABU. A	Quantum Teleportation	16.12.2019	39	39	18	96
8	202008	813817106007	AZEET KUMAR. E	X-Max Technology	16.12.2019	36	38	18	92
9	202009	813817106008	DARSSINI. M	Smart Dust	02.01.2020	36	36	18	90
10	202010	813817106009	DHEEPICAA. P	RFID technology	02.01.2020	38	38	18	94
11	202011	813817106010	DHEEPTHI. G.V	Blue Brain Technology	02.01.2020	36	39	19	94
12	202012	813817106011	DIVYA DARSHINI. P	Neural headset	02.01.2020	38	36	20	94
13	202013	813817106012	ELANCHEZHIAN. E	VPN	02.01.2020	38	38	14	94
14	202014	813817106013	GOPINATH. R	Mobile phone cloning	02.01.2020	35	38	17	90
15	202015	813817106014	GOWTHAM. V	5 G networks	02.01.2020	39	39	18	96
16	202016	813817106015	HAJERA. M	Touchless Technology	02.01.2020	38	38	18	94
17	202017	813817106016	HARDHINI. S A	MEMS technology	02.01.2020	39	38	18	95
18	202018	813817106017	HARINI. J	LIFI technology	02.01.2020	38	39	18	95

19	202019	813817106018	HARISH. R	SATrack	08.01.2020	36	38	18	92
20	202020	813817106019	HARSHIKAA. G	Space Based Solar power	08.01.2020	34	38	18	90
21	202021	813817106020	HELINA JUVAKIN. J	Digital scent technology	08.01.2020	38	38	18	94
22	202022	813817106021	HELIX RICHARD. N	Embedded system in automotive	08.01.2020	38	38	18	94
23	202023	813817106022	HEMACHANTHIKA. R	DAK Network	08.01.2020	39	39	18	96
24	202024	813817106023	INIYAAN. P	Visual Prosthetic	08.01.2020	38	38	18	94
25	202025	813817106024	JANISHA. L	Ingestible Robots	08.01.2020	35	38	17	90
26	202026	813817106025	JAMUNA. R	Smart Quill Pen	08.01.2020	39	39	20	98
27	202029	813817106027	JAYA PRASANTH. M	Optical Coherence Tomography	08.01.2020	35	38	17	90
28	202030	813817106026	JAYANI. N	LoRaWAN	21.01.2020	34	34	20	88
29	202031	813817106028	JAYAPREETHA. R	Satellite Technology	21.01.2020	39	38	18	95
30	202032	813817106029	JEEVANANTHINI. R	Agrobots	21.01.2020	38	38	18	94
31	202033	813817106030	JEROME INFANT RAJ. S	Electronic Waste Management	21.01.2020	37	39	18	94
32	202034	813817106032	JONE STERVIN. F	Brain Fingerprinting	21.07.2020	38	36	18	92
33	202035	813817106033	JOY PRASANNA. J	Bio Metric ATM	21.01.2020	36	36	18	90
34	202037	813817106035	KARTHI VAIDYA. P	E Skin	21.01.2020	37	37	18	92
35	202038	813817106036	KARTHIKA. K	Steganography	21.01.2020	38	38	18	94
36	202039	813817106037	KEERTHANA. K	Wireless Communication	27.01.2020	35	35	18	88
37	202041	813817106039	KIRUTHIGA. E	E textiles	27.01.2020	39	35	18	96
38	202042	813817106040	KISHOR KUMAR. R	Fibre to the home	27.01.2020	39	39	18	92
39	202043	813817106041	LALITHA. S	Medical Mirror	27.01.2020	35	35	18	88
40	202045	813817106043	LEO TAMIZHAZHAGAN. M	Space Telescopes	27.01.2020	38	39	18	95
41	202046	813817106044	LORAMARY. S	LIFI	27.01.2020	36	38	18	92
42	202047	813817106045	MADURABHASHINI. R	3D printing Technology	27.01.2020	39	39	18	96



43	202048	813817106046	MAHALAKSHMI. R	Embedded smart card sequence system	27.01.2020	35	35	18	88
44	202049	813817106047	MANISHA. G	Electronic Toll Connection	01.02.2020	36	38	18	92
45	202050	813817106048	MOHAMMED SATTAR. B	Quantum Computer	01.02.2020	39	39	20	98
46	202051	813817106049	MYTHRAYE. M S	Data Logger	01.02.2020	36	38	18	92
47	202052	813817106050	NANDAKUMAAR. A	Block Chain Management	01.02.2020	35	35	18	88
48	202053	813817106051	NANDHINI. A	Eye Directive Wheel Chair	01.02.2020	36	36	18	90
49	202054	813817106052	NANDHINI. S	Hawk Eye	01.02.2020	35	35	18	88
50	202055	813817106053	NAVAMANI. R.S	Eye Tap	01.02.2020	36	38	18	94
51	202056	813817106058	NIVETHITHA. M	Haptic Technology	01.02.2020	36	36	18	90
52	202057	813817106054	NAVYAA. L	decompression using artificial neural network	07.02.2020	37	38	17	92
53	202058	813817106055	NIKESH. J	Domains of AI	07.02.2020	36	38	18	92
54	202059	813817106056	NITHYA. G	Trending AI in China (Shenzhen)	07.02.2020	39	39	18	96
55	202060	813817106057	NIVETHA. S	Honey pot Technology	07.02.2020	35	38	17	90
56	202061	813817106059	PRABU. S	Quantum space	07.02.2020	39	38	18	95
57	202121	813817106302	HARI JAMANI. R	Smart Camera in Embedded Systems	07.02.2020	35	38	17	90
58	202123	813817106306	VICTORIYA JOSI. A	Smart Voting	07.02.2020	36	36	18	90

  
HOD/ECE

1. Dr. S.A. Arunmozhi S.A. Arunmozhi  
2. Dr. V. Mohan Dr. V. Mohan

  
CO-ORDINATOR

PANEL MEMBERS



**SARANATHAN COLLEGE OF ENGINEERING , TIRUCHIRAPALLI - 620 012.**

**Department of Electrical and Electronics Engineering**

**Time Table Even Semester (Dec - Apr) 2018-2019**

**Sem : IV**  
**Branch : EEE A**  
**Hall : KS101**

**Class Co-ordinator: Mr.R.Balasubramanian**

D.O	1(9.15-10.05)	2(10.05-10.55)	3(11.05-11.55)	4(11.55-12.45)	5(1.25-2.15)	6(2.15-3.05)	7(3.15-4.00)	8(4.00-4.45)
I	EE8403	EE8402	EE8451	EE8401/ EE8402 (T-2)	MA8491(T-1)		EE8411/EE8461	
II	EE8401	IC8451	EE8402	EE8403	EE8451	EE8402	MA8491	EE8401
III	MA8491	T&P(M)	EE8451	IC8451	EE8402	EE8401	EE8403	MA8491
IV	IC8451		EE8411/EE8461		EE8412	EE8401	EE8412	T&P(E)
V	EE8402	IC8451	EE8401	EE8451	IC8451	MA8491	EE8403	EE8451

S.Code	Title	Name of the Staff	No. of Hours
MA8491	Numerical Methods	Ms.N.Subashini (Maths)	4
EE8401	Electrical Machines-II	Mr.P.Ramesh Babu	5
EE8402	Transmission & Distribution	Mr.B.Paranathagan	5
EE8403	Measurements and Instrumentation	Dr.S.Vijayalakshmi	4
EE8451	Linear Integrated Circuits & Applications	Mr.V.Ramya (ECE)	5
IC8451	Control Systems	Mr.R.Balasubramanian	5
EE8411	Electrical Machines Laboratory -II	Mr.B.Paranathagan / Mr.R.Balasubramanian / Mr.P.Ramesh Babu / Mr.P.K.Arun Kumar	3
EE8461	Linear Integrated Circuits Laboratory	Ms.V.Ramya(ECE) / Mr.V.Koushick (ECE) / Ms.G.Lakshmi(ECE)	3
EE8412	Technical Seminar	Dr.S.Vijayalakshmi	2
T&P(E)	Training & Placement	Dr.A.Narayanan (English)	1
T&P(M)	Training & Placement	Dr.D Geetha (Maths)	1
T-1	Numerical Methods	Ms.N.Subashini (Maths) / Dr.D Geetha (Maths)	1
T-2	Transmission & Distribution/ Electrical Machines-II	Mr.B.Paranathagan / Mr.P.Ramesh Babu	1

HOD/EEE

Principal

## PROJECT WORK



**SARANATHAN COLLEGE OF  
ENGINEERING**  
DEPARTMENT OF ELECTRICAL AND ELECTRONICS  
ENGINEERING  
TIRUCHIRAPALLI - 620 012



Date: 19.12.2018

Ref No: SCE/EEE/UG project/Zeroth review schedule/2018-2019/01

The Zeroth Review of final year EEE UG project will be held from 27-12-18 to 29-12-18 at EEE seminar hall. All batches are advised to submit one printed copy of the presentation with Guide signature. (As per the format)

Students should ensure the following.

1. Tentative project title.
2. Project Log book. (Signed from internal guide)
3. Power point preparation emphasize on
  - a. Project Area.
  - b. Objectives.
  - c. Literature survey about the project with reference papers.
  - d. Overview of the project with block diagram.
  - e. Project execution plan.

Project Schedule

S.NO	DATE	PROJECT BATCH NO	Section	TIME
1	27-12-18	1 to 12	A section	9.30 am to 4.30
2	28-12-18	12 to 19	A section	9.30 am to 4.30
		1 to 4	B section	
3	29-12-18	5 to 18	B section	9.30 am to 4.30

Date: ~~19/12/18~~ 19/12/18

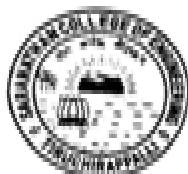
All the students should report well before the commencement of the review on the respective day order failing which he/she is treated as absent

Project Coordinators

1. Mr.S.Ram Prasath
2. Mr.B.Paranthagan
3. Dr.S.Thamizharasan
4. Dr.K.Rajkumar

HOD/EEE





**SARANATHAN COLLEGE OF ENGINEERING**  
**DEPARTMENT OF ELECTRICAL AND ELECTRONICS**  
**ENGINEERING**  
**TIRUCHIRAPALLI - 620 012**

Date: 06.02.2019

Ref No: SCE/EEE/UG project/Second review schedule/2018-2019/03

The Second Review of final year EEE UG project will be held from 18-02-19 to 21-02-19 at EEE seminar hall. All batches are advised to submit one printed copy of the presentation with Guide signature. (As per the format)

Students should ensure the following.

1. Power point preparation should emphasize on
  - a. Detailed Design of proposed system.
  - b. Hardware Results obtained and demo.
2. Project Log book.
3. Preparation of manuscript for publication.(hard copy in IEEE format)
4. U.G Project report status.

Project Schedule

S.NO	DATE	PROJECT BATCH NO	Section	Presentation	Demo
1	18-02-19	1 to 10	A section	9.30 am to 12.30pm	1.45pm to 4.30pm
2	19-02-19	11 to 19	A section	9.30 am to 12.30pm	1.45pm to 4.30pm
3	20-02-19	1 to 10	B section	9.30 am to 12.30pm	1.45pm to 4.30pm
4	21-02-19	11 to 18	B section	9.30 am to 12.30pm	1.45pm to 4.30pm

Date: 06-02-2019

All the students should report well before the commencement of the review on the respective day order failing which he/she is treated as absent

**Project Coordinators**

1. Mr.S.Ram Prasath
2. Mr.B.Paranthagan
3. Dr.S.Thamizharasan
4. Dr.K.Rajkumar

*(Signatures of Project Coordinators)*

*(Signature of HOD/EEE)*  
HOD / EEE



**SARANATHAN COLLEGE OF ENGINEERING**  
**DEPARTMENT OF ELECTRICAL AND ELECTRONICS**  
**ENGINEERING**  
**TIRUCHIRAPALLI - 620 012**

Date: 18.01.2019

Ref No: SCE/EEE/UG project/First review schedule/2018-2019/02

The First Review of final year EEE UG project will be held from 22-01-19 to 25-01-19 at EEE seminar hall. All batches are advised to submit one printed copy of the presentation with Guide signature. (As per the format)

Students should ensure the following,

1. Power point preparation should emphasize on
  - a. Operation and Detailed Design of proposed system.
  - b. Step-by-step algorithms for programs, Simulation / hardware Model Results obtained (Intermediate)
2. Project Log book.
3. List of components Purchased.( bill of materials)

**Project Schedule**

S.NO	DATE	PROJECT BATCH NO	Section	TIME
1	22-01-19	18 to 9	B section	9.30 am to 4.30
2	23-01-19	8 to 1	B section	9.30 am to 4.30
		19 and 18	A section	
3	24-01-19	17 to 8	A section	9.30 am to 4.30
4	25-01-19	7 to 1	A section	9.30 am to 4.30

Date: 18-01-2019

All the students should report well before the commencement of the review on the respective day order failing which he/she is treated as absent

**Project Coordinators**

1. Mr.S.Ram Prasath
2. Mr.B.Paranthagan
3. Dr.S.Thamizharasan
4. Dr.K.Rajkumar

  
HOD / EEE

EEE A and B section

EEE A and B section														
S.No	Batch No	University no	Name	Review marks ( 20)				Thesis ( 30)		Viva(50)				
				no	Review 1	Review 2	Review 3	Total	Internal (15)	External (15)	Internal (15)	External (20)	Supervisor (15)	Total (100)
1	183008	813815105008	AJITH ARUMUGAM.A	1	70	85	50	14	14	14	9	10	5	66
2	183012	813815105012	ASWANTH. B	1	90	85	83	17	14	14	14	18	15	92
3	183056	813815105056	MOHAMED ASIF .K	1	85	85	83	17	14	14	14	15	15	89
4	183122	813815105308	PRADEEP KUMAR M	1	85	50	50	12	14	14	9	10	7	66
6	183129	813815105309	RAGUL. R	2	76	76	81	16	14	13	10	14	14	81
5	183128	813815105310	SARAVANAN. R	2	76	76	70	15	14	13	10	9	10	71
8	183135	813815105311	SRI PAUL. J	2	76	76	70	15	14	13	10	9	14	75
7	183134	813815105313	VIJAYAKUMAR. M	2	76	76	71	15	14	13	10	14	14	80
9	183015	813815105015	BARJANA BEGAMS	3	94	76	96	18	14	14	15	18	15	94
10	183024	813815105023	DHIVYA. A	3	94	76	96	18	14	14	13	18	14	91
11	183048	813815105048	MAHALAKSHMI. C	3	94	76	96	18	14	14	13	16	14	89
12	183054	813815105054	MERLIN ANTONITA. X	3	94	76	96	18	14	14	15	18	14	93
13	183001	813815105001	AADITHYA VIGNESH .G.	4	75	83	85	16	14	14	14	18	14	90
14	183019	813815105019	CHENDURAN. A	4	75	83	85	16	14	14	14	17	14	89
15	183025	813815105025	DINESHKUMAR. V	4	75	85	85	17	14	14	14	16	14	89
16	183055	813815105055	MOHAMED AADIL. J	4	75	85	85	17	14	14	14	18	14	91
17	183038	813815105038	KAVYA.V	5	85	78	95	17	14	14	14	17	14	90
18	183049	813815105049	MAHALAKSHMIS	5	85	78	95	17	14	14	14	18	14	91
19	183053	813815105053	MEENACHILR	5	85	78	95	17	14	14	14	18	15	92
21	183005	813815105005	ABENAYA. T	6	84	78	88	17	13	13	13	15	14	85
20	183027	813815105027	EAZHILARASIJ	6	84	78	88	17	13	13	13	15	13	84
22	183031	813815105031	HEMADEVIL T	6	84	78	88	17	13	13	13	15	14	85
23	183011	813815105011	ARUN PRASATH.P.A	7	85	79	88	17	14	14	14	18	15	92
24	183021	813815105021	CYRIL ROZARIO.B	7	95	79	95	18	14	14	15	18	15	94
25	183043	813815105043	KISHORE KUMAR. S	7	85	78	75	16	14	14	10	15	11	80
26	183127	813815105312	VIGNESH. V	7	85	79	75	16	14	14	10	15	14	83
27	183017	813815105017	BHAVADHARINIP	8	95	77	95	18	14	14	15	18	13	92
28	183030	813815105030	HARITHA.R	8	95	77	95	18	14	14	15	18	13	92
29	183007	813815105007	AJAY .M.S.	9	82	82	82	16	13	13	13	14	14	83
30	183010	813815105010	ARAVINDAN. U	9	84	83	84	17	13	13	15	15	14	87



Saranathan college of engineering  
Department of Electrical and Electronics Engineering  
UG Project Mark Sheet 2018-2019

EEE A and B section

S.No	Batch No	University no	Name	Review marks ( 20)				Thesis ( 30)			Viva(50)		
				no	Review 1	Review 2	Review 3	Total	Internal (15)	External (15)	Internal (15)	External (20)	Supervisor (15)
31	183022	813815105022	DHINESH KUMAR. R	9	82	83	82	16	13	13	13	14	12
32	183050	813815105050	MAHENDARAN.P	9	75	78	75	15	13	13	13	12	14
33	183013	813815105013	AVINASH.T	10	50	0	50	6	10	10	14	11	15
34	183057	813815105057	MOHAMMED FAISAL .Z.	10	50	0	50	6	10	10	14	10	15
35	183059	813815105059	MUTHU KUMAR.S	10	50	0	50	6	10	10	14	10	15
36	183123	813815105301	AJITH. E	10	50	0	50	6	10	10	14	12	15
37	183003	813815105003	A.ATHREYA. S	11	88	81	95	18	14	14	14	18	14
38	183036	813815105036	KARTHIKHAJINI. K	11	88	80	95	18	14	14	14	16	14
39	183039	813815105039	KEERTHANA. V	11	88	80	95	18	14	14	14	16	14
40	183044	813815105044	KRISHNA PRIYA. V	11	88	83	95	18	14	14	15	18	14
41	183026	813815105026	DIVYA.S	12	75	76	80	15	13	13	13	13	12
42	183034	813815105034	KARTHIKA. V	12	75	76	80	15	13	13	13	13	13
43	183042	813815105042	KIRUTHIKA. P	12	75	76	80	15	13	13	13	13	12
44	183014	813815105014	BARATH. R	13	89	82	95	18	14	14	14	16	14
45	183016	813815105016	BHARANIDHARAN. M.S	13	89	82	95	18	14	14	14	17	14
46	183018	813815105018	BOOPATHINAVANEETHAN. S	13	70	82	70	15	14	14	10	8	10
47	183045	813815105045	LAKSHMI NARAYANAN. T	13	89	82	95	18	14	14	14	17	14
48	183046	813815105046	LEO ALDREINRAJ.A.S	14	90	80	94	18	14	14	14	17	14
49	183047	813815105047	MADHANRAJ. S	14	90	80	94	18	14	14	14	17	14
50	183052	813815105052	MANORANJAN.G	14	90	80	94	18	14	14	12	10	12
51	183058	813815105058	MUTHU KARUPPAN .N.	14	90	80	94	18	14	14	14	17	14
52	183002	813815105002	AARTHY. A	15	95	85	94	18	14	14	15	18	15
53	183009	813815105009	AKHILA.B	15	95	85	94	18	14	14	15	18	15
54	183020	813815105020	CHIVUKULA SRILAXMIE	15	95	82	94	18	14	14	15	18	15
55	183029	813815105029	GAYATHRI. N	15	95	85	94	18	14	14	15	18	15
56	183070	813815105070	PAVITHRA SHRI .S	16	80	71	85	16	14	14	13	15	12
57	183104	813815105104	SUBALAKSHMI.K	16	95	79	94	18	14	14	14	18	14
58	183111	813815105111	SWETHA. S	16	95	79	94	18	14	14	14	18	14
59	183114	813815105114	THARANYA. S	16	95	72	94	17	14	14	14	17	14
60	183065	813815105065	NAVEENKUMAR. M	17	96	76	95	18	14	14	14	18	14

**Saranathan college of engineering**  
**Department of Electrical and Electronics Engineering**  
**UG Project Mark Sheet 2018-2019**

**EEE A and B section**

EEE A and B section														
S.No	Batch No	University no	Name	Review marks ( 20)				Thesis ( 30)		Viva(50)				
				no	Review 1	Review 2	Review 3	Total	Internal (15)	External (15)	Internal (15)	External (20)	Supervisor (15)	Total (100)
61	183072	813815105072	POOVARASAN. R	17	96	76	95	18	14	14	14	18	14	92
62	183079	813815105079	PRITHIVIRAJAN.E	17	96	76	95	18	14	14	14	18	14	92
63	183086	813815105086	RAMANATHAN.AL	17	96	76	95	18	14	14	14	18	14	92
64	183006	813815105006	ABISHEK. S.	18	95	82	95	18	14	14	14	17	14	91
65	183028	813815105028	ELAMURUGU. K	18	95	82	95	18	14	14	14	17	14	91
66	183023	813815105024	DHIVYA BHARATHI .A	19	90	70	94	17	14	14	12	18	14	89
67	183037	813815105037	KAVIPRIYA. V.R	19	90	70	94	17	14	14	12	18	14	89
68	183060	813815105060	MUTHULAKSHMI. M	19	90	70	94	17	14	14	12	18	14	89
69	183126	813815105305	ISHWARYA. S	19	90	70	94	17	14	14	12	18	14	89
70	183087	813815105087	RAMYAPRABHA. R	20	95	82	95	18	14	14	14	18	14	92
71	183089	813815105089	REKHA. L	20	95	78	95	18	14	14	14	18	14	92
72	183090	813815105090	SAJITHA NILOFER.S	20	95	75	95	18	14	14	14	18	14	92
73	183101	813815105101	SRIMATHI. S	20	95	83	95	18	14	14	14	18	14	92
74	183073	813815105073	PRADEEP RAJ. K	21	95	84	95	18	14	14	15	18	15	94
75	183074	813815105074	PRASANTH. P	21	95	84	95	18	14	14	15	18	15	94
76	183100	813815105100	SOORYA. B	21	95	84	95	18	14	14	14	18	14	92
77	183124	813815105304	HARIPRAKASH. R	21	95	84	95	18	14	14	14	18	14	92
78	183082	813815105082	RAGHAVAN. S.S	22	96	90	96	19	14	14	15	18	15	95
79	183107	813815105107	SWAMINATHAN.S	22	96	90	96	19	14	14	15	18	15	95
80	183119	813815105119	VISHAL. N	22	96	90	96	19	14	14	15	18	15	95
81	183064	813815105064	NAVEEN KUMAR. K	23	90	75	80	16	12	12	12	16	14	82
82	183083	813815105083	RAHUL MALLYA	23	90	75	80	16	12	12	12	16	14	82
83	183088	813815105088	RANJITH KUMAR.A	24	80	76	85	16	12	12	13	18	14	85
84	183093	813815105093	SATHISHKUMAR. S	24	75	71	75	15	12	12	12	12	13	76
85	183125	813815105303	ARAVINTHAN. M	24	70	70	70	14	10	10	10	9	10	63
86	183130	813815105306	MAHENDRAN. S	24	70	74	70	14	10	10	10	9	10	63
87	183063	813815105063	NANDA. S.	25	96	77	96	18	14	14	15	18	15	94
88	183077	813815105077	PRINCE THOMSON.J	25	96	77	96	18	14	14	15	18	15	94
89	183098	813815105098	SIVA SHANKAR. M.R	25	96	77	96	18	14	14	15	18	15	94
90	183068	813815105068	PARVATHI.K	26	95	80	95	18	15	14	15	18	15	95

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S.No	Batch No	University no	Name	Review marks ( 20)				Thesis ( 30)		Viva(50)				
				no	Review 1	Review 2	Review 3	Total	Internal (15)	External (15)	Internal (15)	External (20)	Supervisor (15)	Total (100)
91	183076	813815105076	PRATHEEBA. R	26	95	81	95	18	15	14	15	18	15	95
92	183115	813815105115	UDHAYANIRANJANA .V	26	95	81	95	18	15	14	15	18	15	95
93	183095	813815105095	SHAHID HASSAN .T	27	95	77	95	18	14	14	15	18	14	93
94	183103	813815105103	SRINIVASAN .SB.	27	95	78	95	18	14	14	15	18	14	93
95	183106	813815105106	SURESH KUMAR. M	27	95	76	95	18	14	14	15	18	14	93
96	183118	813815105118	VINOTHRAJ .R.T	27	80	75	80	16	14	14	12	13	14	83
97	183066	813815105066	NIVEDHA. M	28	85	73	80	16	14	14	13	14	14	85
98	183092	813815105092	SARANYA.U	28	88	73	95	17	14	14	15	18	14	92
99	183094	813815105094	SELVARANI. G	28	85	73	80	16	14	14	14	14	14	86
100	183071	813815105071	POOJA SRIB	29	95	83	95	18	14	14	15	18	15	94
101	183080	813815105080	PRIYADHARSHINI. K	29	95	82	95	18	14	14	15	18	15	94
102	183097	813815105097	SINDHU ROSHNI .R	29	95	82	95	18	14	14	15	18	15	94
103	183041	813815105041	KIRUTHIKA .G.	30	95	81	95	18	14	14	14	18	15	93
104	183081	813815105081	RAGAVI .S.	31	85	75	85	16	14	14	14	13	14	85
105	183091	813815105091	SANGEETHA .N	31	85	75	84	16	14	14	14	12	14	84
106	183110	813815105112	SWETHA GAYATHRI. S	31	85	75	85	16	14	14	14	12	14	84
107	183116	813815105116	VIJAYA LAKSHMI .K	31	85	75	85	16	14	14	14	13	14	85
108	183085	813815105085	RAMAIAH.C	32	65	69	0	8	10	10	10	10	14	62
109	183069	813815105069	PAVINTHAN. M	33	95	80	95	18	14	14	15	18	15	94
110	183099	813815105099	SIVASUBRAMANIAN.S	33	95	82	95	18	14	14	15	18	15	94
111	183105	813815105105	SURENDRAN. K.R	33	95	80	95	18	14	14	15	18	15	94
112	183131	813815105302	AJITHKUMAR. A	33	95	80	95	18	14	14	8	8	9	71
113	183062	813815105062	MYTHILIS	34	95	85	96	18	14	14	15	18	15	94
114	183075	813815105075	PRASANTHINI.S	34	95	85	96	18	14	14	15	18	15	94
115	183096	813815105096	SHANTHI .L	34	95	85	96	18	14	14	15	18	15	94
116	183117	813815105117	VIJITHRA .N	34	95	85	96	18	14	14	15	18	15	94
117	183067	813815105067	NIVETHA.R	35	94	77	94	18	14	14	14	18	14	92
118	183108	813815105108	SWARNA GOWRI. M	35	94	77	94	18	14	14	14	18	14	92
119	183112	813815105110	SWETHA.S	35	94	77	94	18	14	14	14	18	14	92
120	183120	813815105120	VISHALI.G	35	94	77	94	18	14	14	14	18	14	92

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S.No	Batch No	University no	Name	Review marks ( 20)				Thesis ( 30)		Viva(50)		
				no	Review 1	Review 2	Review 3	Internal (15)	External (15)	internal (15)	External (20)	Supervisor (15)
121	183004	813815105004	ABINAYA. K	36	96	77	94	18	14	14	18	15
122	183032	813815105032	JANE MIRIAM.J	36	96	78	92	18	14	14	18	14
123	183033	813815105033	KALAISELVI. K	36	96	77	93	18	14	14	18	14
124	183040	813815105040	KIRTHIKA.R	36	96	77	93	18	14	14	18	15
125	183102	813815105102	SRIMATI .G	37	94	87	94	18	14	14	18	14
126	183078	813815105078	PRINCY SMITHA. G	38	95	79	95	18	14	14	19	15
127	183109	813815105109	SWATHI.A	38	95	79	95	18	14	14	18	14
128	183113	813815105113	THARANI. R	38	95	78	95	18	14	14	18	15
129	183121	813815105121	YAMUNA.G	38	95	78	95	18	14	14	18	13